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GENERAL

INTRODUCTION

SCOPE OF THIS MANUAL

This manual is intended for use by experienced technicians familiar with similar types of commercial grade communications equipment. It contains all required service information for the equipment and is current as of this publication date. Changes which may occur after publication are covered by either Service Bulletins or Manual Revisions. These are issued as required.

ORDERING REPLACEMENT PARTS

When ordering replacement parts or equipment information, the full part identification number should be included. This applies to all parts : components, kits, and chassis. If the part number is not known, include the chassis or kit number of which it is a part, and a sufficient description of the required component for proper identification.

PERSONNEL SAFETY

The following precautions are recommended for personnel safety :

- DO NOT transmit if someone is within two feet (0.6 meter) of the antenna.
- DO NOT transmit until all RF connectors are verified secure and any open connectors are properly terminated.
- SHUT OFF and DO NOT operate this equipment near electrical blasting caps or in an explosive atmosphere.
- All equipment should be properly grounded before power-up for safe operation.
- This equipment should be serviced by a qualified technicians only.

PRE-INSTALLATION CONSIDERATIONS

1. UNPACKING

Unpack the radio from its shipping container and check for accessory items. If any item is missing, please contact KENWOOD immediately.

2. LICENSING REQUIREMENTS

Federal regulations require a station license for each radio installation (mobile or base) be obtained by the equipment owner. The licensee is responsible for ensuring transmitter power, frequency, and deviation are within the limits permitted by the station license.

Transmitter adjustments may be performed only by a licensed technician holding an FCC first, second or general class commercial radiotelephone operator's license. There is no license required to install or operate the radio.

3. PRE-INSTALLATION CHECKOUT

3-1. Introduction

Each radio is adjusted and tested before shipment. However, it is recommended that receiver and transmitter operation be checked for proper operation before installation.

3-2. Testing

The radio should be tested complete with all cabling and accessories as they will be connected in the final installation. Transmitter frequency, deviation, and power output should be checked, as should receiver sensitivity, squelch operation, and audio output. QT equipment operation should be verified.

4. PLANNING THE INSTALLATION

4-1. General

Inspect the vehicle and determine how and where the radio antenna and accessories will be mounted.

Plan cable runs for protection against pinching or crushing wiring, and radio installation to prevent overheating.

4-2. Antenna

The favored location for an antenna is in the center of a large, flat conductive area, usually at the roof center. The trunk lid is preferred, bond the trunk lid and vehicle chassis using ground straps to ensure the lid is at chassis ground.

4-3. Radio

The universal mount bracket allows the radio to be mounted in a variety of ways. Be sure the mounting surface is adequate to support the radio's weight. Allow sufficient space around the radio for air cooling. Position the radio close enough to the vehicle operator to permit easy access to the controls when driving.

4-4. DC Power and wiring

1. This radio may be installed in negative ground electrical systems only. Reverse polarity will cause the cable fuse to blow. Check the vehicle ground polarity before installation to prevent wasted time and effort.
2. Connect the positive power lead directly to the vehicle battery positive terminal. Connecting the Positive lead to any other positive voltage source in the vehicle is not recommended.

CAUTION

If DC power is to be controlled by the vehicle ignition switch, a switching relay should be used to switch the positive power lead. The vehicle ignition switch then controls DC to the relay coil.

3. Connect the ground lead directly to the battery negative terminal.
4. The cable provided with the radio is sufficient to handle the maximum radio current demand. If the cable must be extended, be sure the additional wire is sufficient for the current to be carried and length of the added lead.

5. INSTALLATION PLANNING – CONTROL STATIONS

5-1. Antenna system

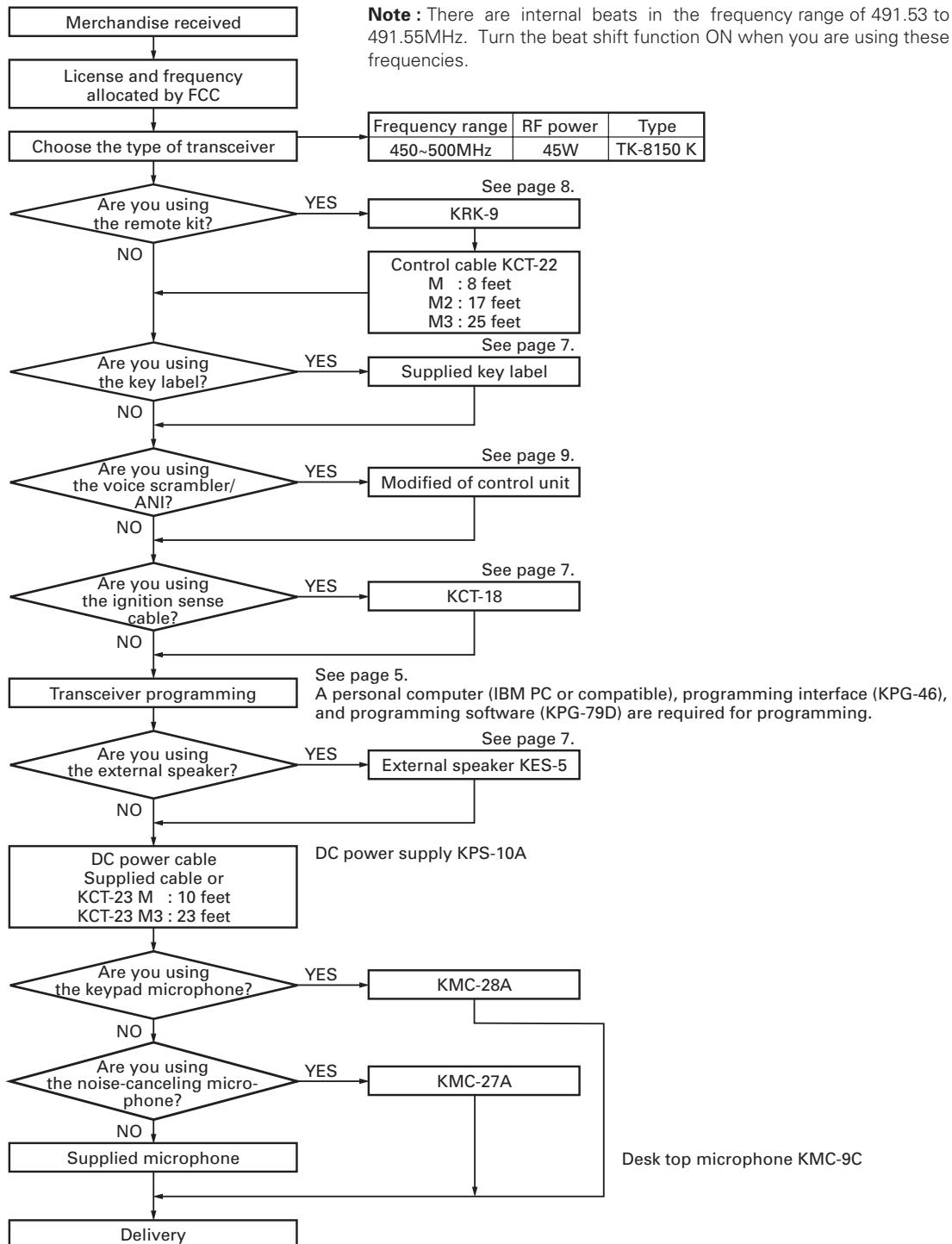
Control station. The antenna system selection depends on many factors and is beyond the scope of this manual. Your KENWOOD dealer can help you select an antenna system that will best serve your particular needs.

GENERAL / SYSTEM SET-UP

5-2. Radio location

Select a convenient location for your control station radio which is as close as practical to the antenna cable entry point. Secondly, use your system's power supply (which supplies the voltage and current required for your system). Make sure sufficient air can flow around the radio and power supply to allow adequate cooling.

SYSTEM SET-UP



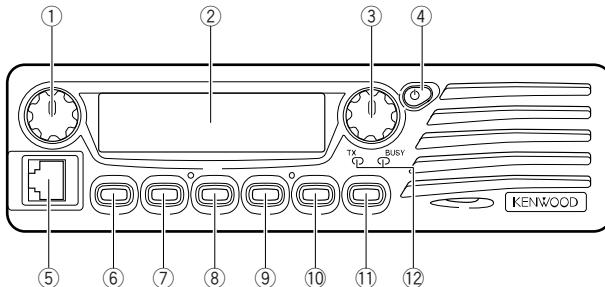
SERVICE

This radio is designed for easy servicing. Refer to the schematic diagrams, printed circuit board views, and alignment procedures contained in this manual.

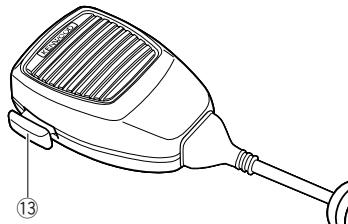
OPERATING FEATURES

1. Controls and Functions

1-1. Front Panel



1-2. Microphone



① Volume Control

Rotate to adjust the volume level. Clockwise increases the volume and counterclockwise decreases the volume.

② Display

(See right.)

③ Selector

Rotate to select a Zone or CH/GID (channel/group ID). Clockwise increases the Zone / CH/GID and counterclockwise decreases the Zone / CH/GID. The default setting is CH/GID Up/Down.

④ ⚡ (Power) switch

Press to switch the transceiver ON. Press and hold for approximately 1 second to switch the transceiver OFF.

⑤ Microphone Jack

Insert the microphone plug into this jack.

⑥ PF1 Key

Press this key to activate its programmable auxiliary function. The default setting is Zone Up.

⑦ PF2 Key

Press this key to activate its programmable auxiliary function. The default setting is Zone Down.

⑧ PF3 Key

Press this key to activate its programmable auxiliary function. The default setting is None.

⑨ PF4 Key

Press this key to activate its programmable auxiliary function. The default setting is None.

⑩ PF5 Key

Press this key to activate its programmable auxiliary function. The default setting is None.

⑪ PF6 Key

Press this key to activate its programmable auxiliary function. The default setting is None.

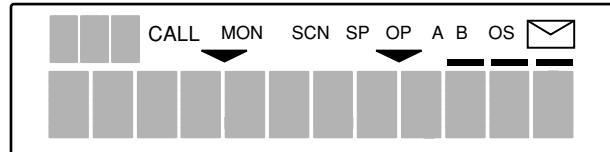
⑫ TX / BUSY LEDs

The TX LED lights red while transmitting. In Conventional Groups, the BUSY LED lights green while receiving.

⑬ PTT switch

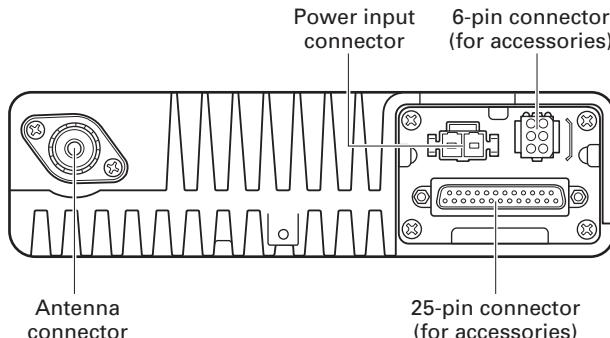
To transmit, press and hold this switch, then speak into the microphone. Release to receive.

1-3. Display



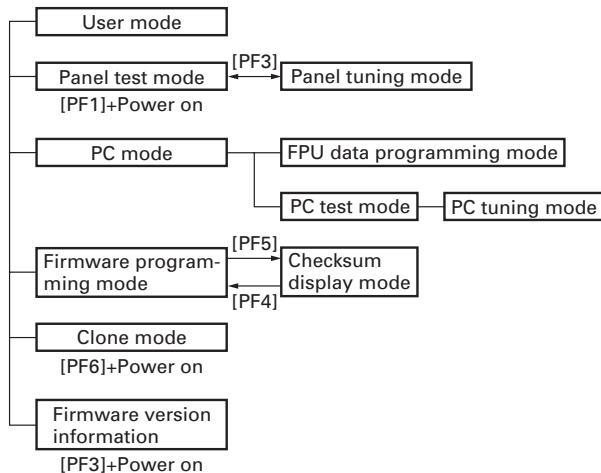
Indicator	Description
☰	Displays the zone, group ID, and channel numbers. Also displays various functions which have been programmed by your dealer.
CALL	Appears when the squelch opens during a 2-Tone or DTMF call.
MON	Appears when the key programmed as Monitor is pressed.
▼ (left side)	Appears when the selected Zone is added to the scan list.
SCN	Appears when you are using Scan mode.
SP	Appears when you are using an external speaker.
OP	Appears when the optional scrambler board is installed and activated.
▼ (right side)	Appears when the selected Channel/Group ID is added to the scan list.
A	Appears when the auxiliary A function is activated.
B	Appears when the auxiliary B function is activated.
OS	Appears when Operator Selectable Tone is activated.
✉	Appears when a FleetSync message is stored in the transceiver memory. Appears and blinks when a new message is received.
☰	Displays the zone, group ID, and channel numbers. Your dealer can program the zone, group ID, and channel names with up to 12 characters, in place of numbers.

1-4. Rear Panel



REALIGNMENT

1. Modes



Mode	Function
User mode	For normal use.
Panel test mode	Used by the dealer to check the fundamental characteristics.
Panel tuning mode	Used by the dealer to tune the radio.
PC mode	Used for communication between the radio and PC (IBM compatible).
FPU data programming mode	Used to read and write frequency data and other features to and from the radio.
PC test mode	Used to check the radio using the PC. This feature is included in the FPU. See panel tuning.
Firmware programming mode	Used when changing the main program of the flash memory.
Clone mode	Used to transfer programming data from one radio to another.
Firmware version information	Used to confirm the internal firmware version.

2. How to Enter Each Mode

Mode	Operation
User mode	Power ON
Panel test mode	[PF1]+Power ON
Panel tuning mode	[Panel test mode]+[PF3]
PC mode	Received commands from PC
Firmware programming mode	[PF2]+Power ON
Checksum display mode	[Firmware programming mode]+[PF5]
Clone mode	[PF6]+Power ON
Firmware version information	[PF3]+Power ON (one second)

3. Panel Test Mode

Setting method refer to "ADJUSTMENT".

4. Panel Tuning Mode

Setting method refer to "ADJUSTMENT".

5. PC Mode

5-1. Preface

The transceiver is programmed by using a personal computer, programming interface (KPG-46) and programming software (KPG-79D).

The programming software can be used with an IBM PC or compatible. Figure 1 shows the setup of an IBM PC for programming.

5-2. Connection Procedure

1. Connect the transceiver to the personal computer with the interface cable.
2. When the Power switch on, user mode can be entered immediately. When PC sends command the radio enter PC mode, and "PROGRAM" is displayed on the LCD. When data transmitting from transceiver, the red LED is blinking. When data receiving to transceiver, the green LED is blinking.

5-3. KPG-46 Description

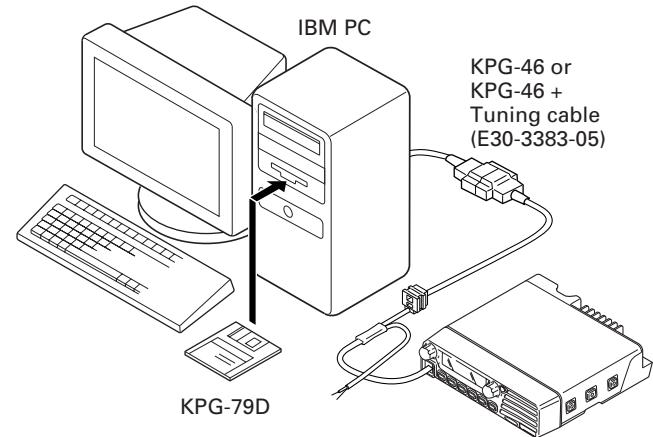
(PC programming interface cable : Option)

The KPG-46 is required to interface the transceiver to the computer. It has a circuit in its D-subconnector (25-pin) case that converts the RS-232C logic level to the TTL level.

The KPG-46 connects the modular microphone jack of the transceiver to the computers RS-232C serial port.

5-4. Programming Software Description

The KPG-79D programming disk is supplied in 3-1/2" disk format. The software on this disk allows a user to program the transceiver radio via programming interface cable (KPG-46).



TK-8150

Fig. 1

5-5. Programming With IBM PC

If data is transferred to the transceiver from an IBM PC with the KPG-79D, the destination data (basic radio information) for each set can be modified. Normally, it is not necessary to modify the destination data because their values are determined automatically when the frequency range (frequency type) is set.

The values should be modified only if necessary.

Data can be programmed into the flash memory in RS-232C format via the modular microphone jack.

KPG-79D instruction manual parts No. : B62-1588-XX.

6. Firmware Programming Mode

6-1. Preface

The TK-8150 uses flash memory to allow it to be easily upgraded when new features are released in the future.

6-2. Connection Procedure

Connect the TK-8150 to the personal computer (IBM PC or compatible) with the interface cable (KPG-46). (Connection is the same as in the PC mode.)

Note :

You can only program firmware from the 8-pin microphone connector on the front panel. Using the 25-pin logic interface on the rear panel will not work.

6-3. Programming

1. Start up the programming software (Fpro. exe).
2. Set the communications speed (normally, 57600 bps) and communications port in the configuration item.
3. Set the firmware to be updated by file name item.
4. Turn ON the transceiver while pressing and holding the [PF2] key. The transceiver enters Firmware programming mode and "PROG 57600". If Firmware programming mode is inhibited by the FPU, the transceiver enters User mode instead.
If the transceiver receives the firmware data from a PC, "PG" appears on the display.
5. Check the connection between the TK-8150 and the personal computer, and make sure that the TK-8150 is in the program mode.
6. Press write button in the window. A window opens on the display to indicate progress of writing.
7. If writing ends successfully, the TX LED on the TK-8150 lights.
8. If you want to continue programming other TK-8150, repeat steps 3 to 6.

Note :

This mode cannot be entered if the firmware programming mode is set to disable in the programming software (KPG-79D).

6-4. Function

Each time you press the [PF4] key, the data transfer rate alters (19200 / 38400 / 57600 bps).

Note :

Normally, write in the high-speed mode (57600 bps).

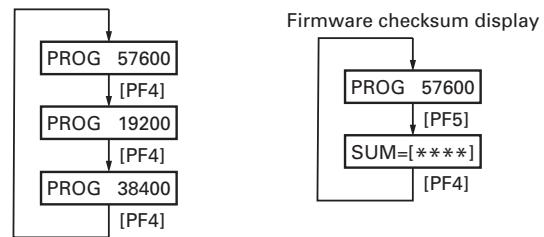


Fig. 2

7. Clone Mode

Programming data can be transferred from one radio to another by connecting them via their modular microphone jacks. The operation is as follows (the transmit radio is the master and the receive radio is a slave).

1. Turn the master TK-8150 power ON with the [PF6] key held down. The TK-8150 displays "CLONE". If Firmware programming mode is inhibited by the FPU, the transceiver enters User mode instead.
2. Power on the slave TK-8150.
3. Connect the cloning cable (No. E30-3382-05) to the modular microphone jacks on the master and slave.
4. Press the [PF6] key on the master while the master displays "CLONE". The data of the master is sent to the slave. While the slave is receiving the data, "PROGRAM" is displayed. When cloning of data is completed, the master displays "END", and the slave automatically operates in the User mode. The slave can then be operated by the same program as the master.
5. The other slave can be continuously cloned. When the [PF6] key on the master is pressed while the master displays "END", the master displays "CLONE". Carry out the operation in steps 2 to 4.
6. To end cloning, first you must remove the cloning cable, then switch both master and slave TK-8150s off.

Note :

Only the same models can be cloned together.

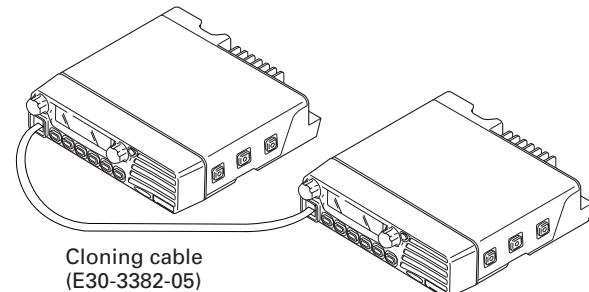


Fig. 3

8. Firmware Version Information

This is a mode to confirm the internal firmware version.

1. Turn the transceiver ON while pressing the [PF3] key. The firmware version will appear on the LCD. When you release the [PF3] key, the transceiver automatically enters User mode.

INSTALLATION

1. Installing Name Plate

Punch out the name plate card. Then insert the plates onto the relative function keys.

You can reconfigure the name plates at any time.

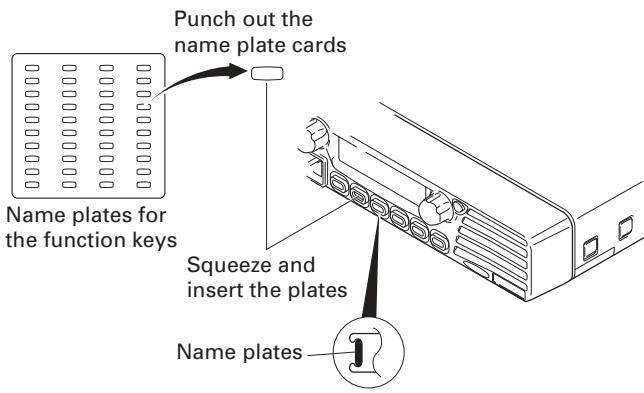


Fig. 1

2. Ignition Sense Cable (KCT-18)

The KCT-18 is an optional cable to use the following functions;

2-1. Ignition Function

The ignition function allows you to turn the transceiver's power on and off with the ignition key of your car. When you are driving with the ignition key on, the horn alert function is disabled.

2-2. Timed Power Off Function

The timed power off function turns the transceiver's power off the time specified with the programming software (KPG-79D) after the ignition key is turned off. When you are driving with the ignition key on, the horn alert function is disabled.

The ignition sense function and the timed power off function can be used at the same time.

2-3. Modification

1. Remove the short plug from the accessory connector (6 pins) on the rear of the transceiver.
2. Cut off the end of the protective cover (accessory), insert the KCT-18 into the protective cover, and insert it into pin 1 (IGN) of the short plug.
3. Install the short plug and protective cover on the connector on the rear of the transceiver, then clamp the bottom of the protective cover with the supplied tie wrap.

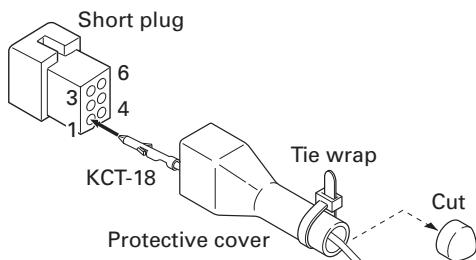


Fig. 2

3. External Speaker (KES-5)

The external speaker output from the accessory connector (6 pin) on the rear of the transceiver is 13W/4Ω. Use the KES-5.

3-1. Connection for the KES-5 with the TK-8150

■ When taking the AF output from the accessory connector (6-pin) on the rear of the radio

The following tools are required for changing the connector.

• Extracting tool

The following extracting tool is recommended;
Molex inc. Order No. : J5800-002 (W05-0878-00)

1. Remove the connector with jumper from the external speaker connector on the rear panel of the radio (Fig. 3-1).
2. Remove the terminals with the jumper from the connector housing holes number 5 and 6 using the extracting tool.

Removing the jumper lead (Fig. 3-2)

- 1) Insert the extracting tool (J5800-002) into the connector while pushing the jumper lead in the direction of (a).
- 2) Push the extracting tool into collapse the barbs of the crimp terminal.
- 3) Pull out the lead while continuing to push the extracting tool in the direction (b).
3. Cut off the end of the protection cover, insert the KES-5 speaker cable into the protective cover.
4. Reinsert the terminal with the black and white stripe lead into hole number 5, and the terminal with the black lead into hole number 4 (Fig. 3-3).
5. Install the plug and protective cover on the accessory connector on the rear of the transceiver, then clamp the bottom of the protective cover with the supplied tie wrap.

Square-type plug
(E37-1031-05)

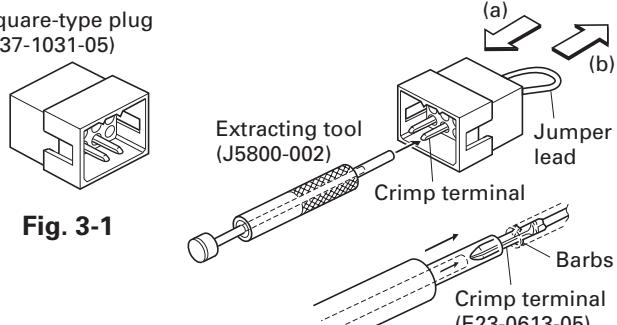


Fig. 3-1

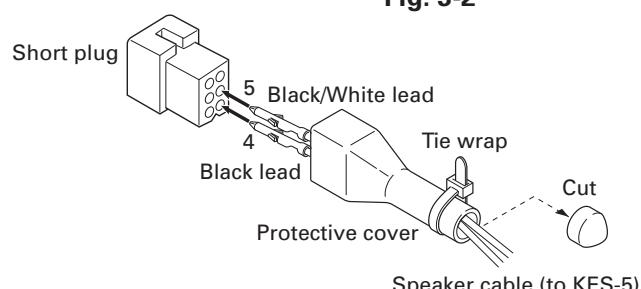


Fig. 3-3

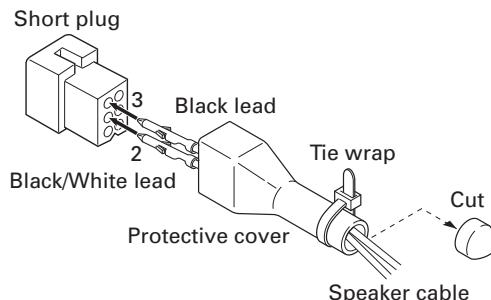
INSTALLATION

4. Use as Public Address Speaker

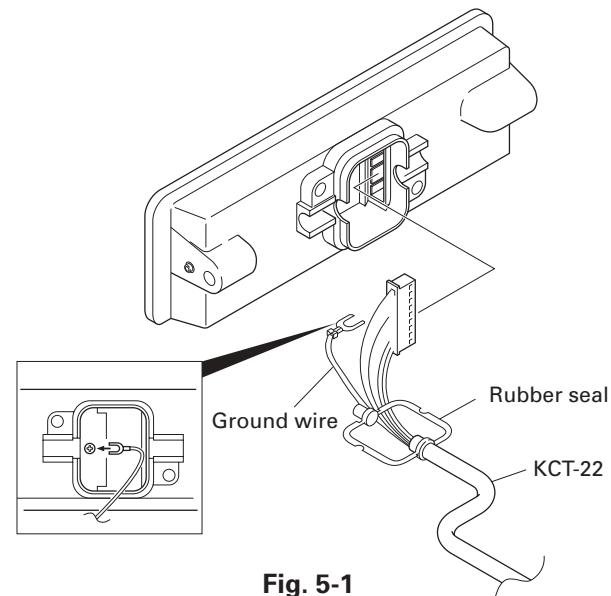
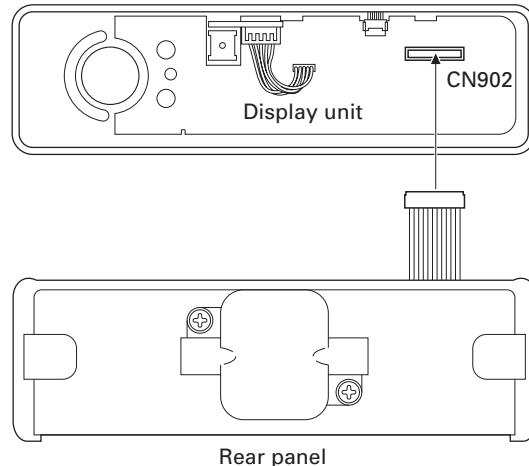
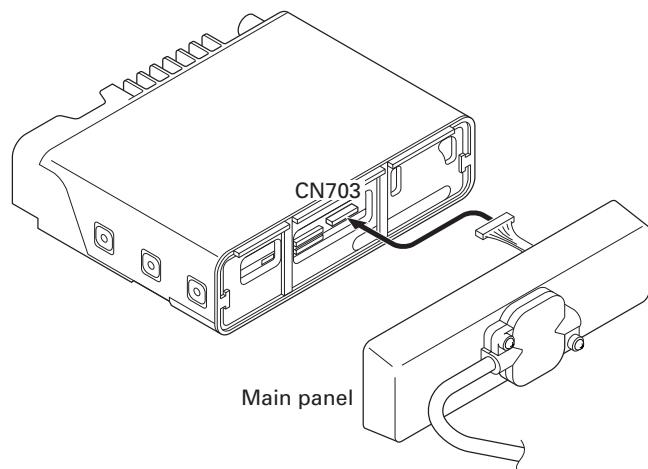
1. Remove the short plug from the 6-pin accessory connector on the rear of the radio. (Remove the jumpers as described in Section 3-1.)
2. Cut off the end of the protective cover, insert the speaker cable into the protective cover, and insert it into pins 2 and 3.
3. Install the plug and protective cover on the accessory connector on the rear of the transceiver, then clamp the bottom of the protective cover with the supplied tie wrap.
4. If you remove jumper shorting pins 5 and 6, the 20W PA (public address) voice signal is output from pins 2 and 3. (Only when the PA or SP switch is on.)
5. If you use the radio with pins 5 and 6 shorted, the internal speaker is available.

Notes :

- Relation ship between accessory connector (6-pins) connection and speaker output.
- When pins 5 and 6 are shorted; The internal speaker is used.
- When pins 5 and 6 are open and output is from pins 2 and 3; The 20W external speaker is used.

**Fig. 4****5. Single Control Head Remote Kit (KRK-9) and Control Cable (KCT-22)**

1. Lift the tab on the bottom of the transceiver, then pull the panel away from the transceiver.
2. Remove the connector that binds the display unit to the TX-RX unit.
3. As shown in Figure 5-1, make sure that the rubber seal is placed above the cable, then plug the 11-pin connector into the front panel PCB assembly.
4. Also, affix the ground wire to the front panel chassis, as shown in Figure 5-1, with the supplied screw.
5. Choose the remote wire position (right side or left side), then place the seal within the guide rail. Attach and secure the cover using the 2 binding screws.
6. Plug the 12-pin connector (from the rear panel) to the CN902 socket on the display PCB, as shown in Figure 5-2.
7. Push and secure the panel into the chassis so that the 6 tabs on the top and bottom are securely fixed.
8. Plug the connector from the main panel into the CN703 socket (Figure 5-3).
9. Push and secure the main panel so that the 6 tabs on the top and bottom of the panel are securely fixed.

**Fig. 5-1****Fig. 5-2****Fig. 5-3**

INSTALLATION

6. Optional Board

6-1. Voice Scrambler Board Connection

■ Modification

1. Remove the cabinet and shielding cover from the transceiver.
2. Remove the panel.
3. Delete R798 and R866 on the TX-RX unit.

■ Connection

The functions of pins of CN701 on the foil side of the TX-RX unit are shown in the figure.

Join the CN701 connector to the voice scrambler board via the E37-0808-05 connector cable.

Note : You must setup the FPU.

The Voice Scrambler Board is connected subsequent to the de-emphasis circuit.

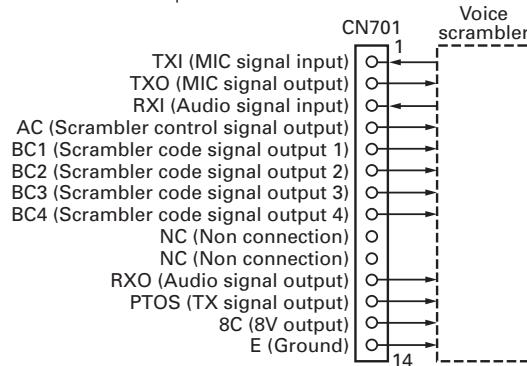


Fig. 6-1 Foil side of the TX-RX unit

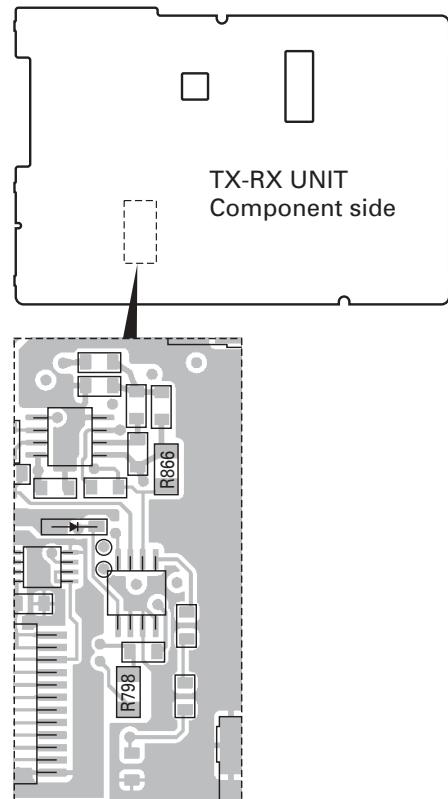


Fig. 6-2

6-2. ANI Board Connection

■ Modification

1. Remove the cabinet and shielding cover from the transceiver.
2. Remove the panel.

■ Connection

Join the CN700 connector to the ANI board via the E37-0808-05 connector cable.

When the ANI board QE-2 is used, you must also use the E37-1075-05 connector.

Note : You must setup the FPU.

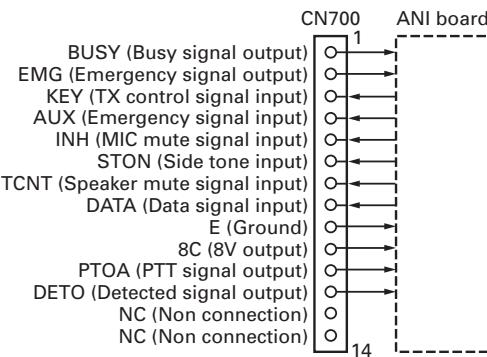
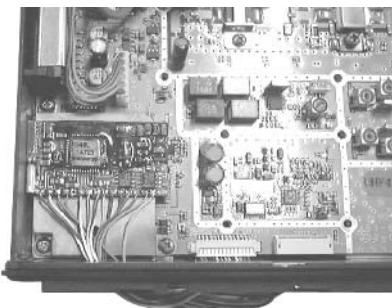


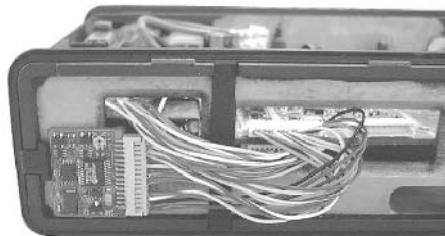
Fig. 6-3 Component side of the TX-RX unit

6-3. Example for Wire Connection

■ Picture (Scrambler board and ANI board)



Top view



Front view

INSTALLATION

7. Wiring of the Cable for Installing the Built-in Type GPS Unit or Other PC Board

1. Loosen the 2 screws (①) to remove the D-sub 25-pin connector.
2. Looses the 4 screws (②) to remove the Hardware fixture.
3. Remove the CN704 connector with the 6-pin lead wire.
4. Take out the Hardware fixture.
5. Cut the packing as shown in the illustration.
7. Tighten the 4 screws to attach the Hardware fixture.
8. Tighten the 2 screws to fix attach D-sub 25-pin connector.

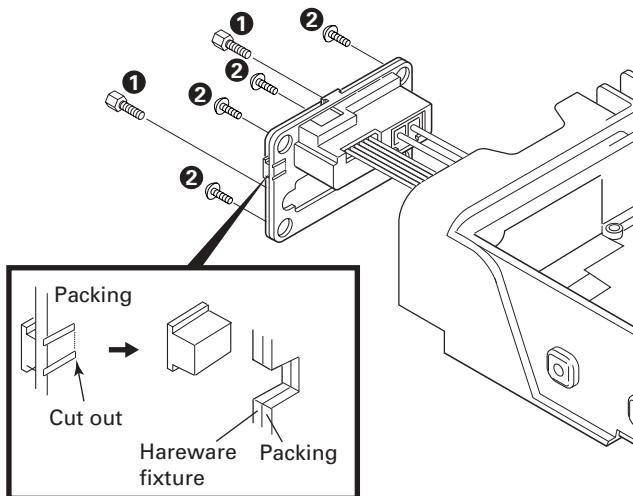


Fig. 7-1

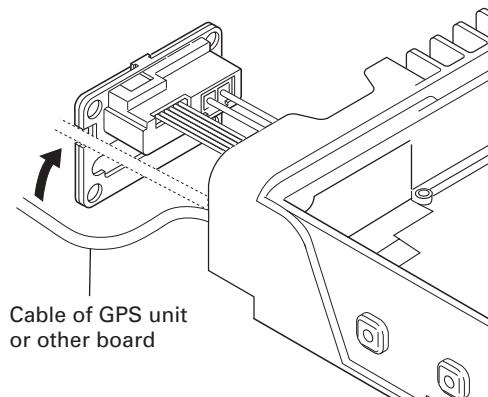


Fig. 7-2

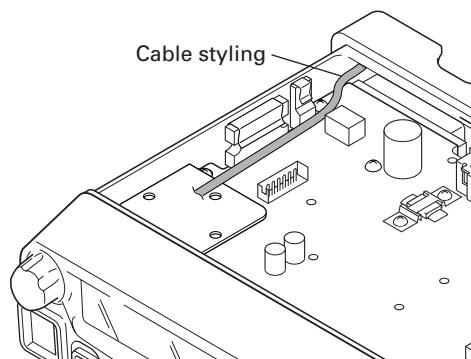


Fig. 7-3

8. Horn Alert Function

The HR1 and HR2 pin of the accessory connector (25-pin) on the rear of the transceiver is a relay and maximum current is 1A.

8-1. Modification

1. Remove the cabinet and shielding cover from the transceiver.
2. Remove the panel.
3. Remove the short plug from the 6-pin accessory connector on the rear of the transceiver.
4. Delete R754 on the TX-RX unit.

	Default	Modification
R754	Enable	Disable
State		

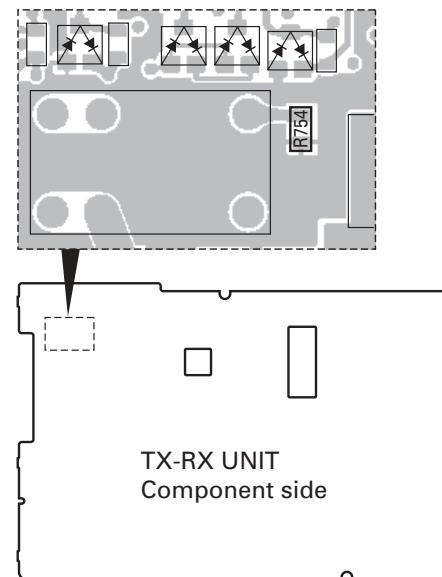


Fig. 8

INSTALLATION

9. Special Instructions for Replacing the Final Amplifier Q5

To replace the Final Amp Q5, ensure to follow the instructions given below:

9-1. C77 and C78 Positions

1. Ensure to solder C77 and C78 to securely contact Q5.

Note : The TX power may vary depending on the position of C77 and C78.

9-2. Soldering

1. Ensure to solder C77 and C78 with the earth spring legs as illustrated in Fig. 9-3.

Note : Use excessive solder to make it thicker than the electrodes of the chip.

2. Solder the terminals until the electrodes of the chip are fully covered. (Refer to Fig. 9-1.)

Note : Use excessive solder to make it thicker than the electrodes of the chip. (Refer to Fig. 9-2.)

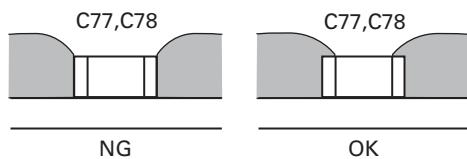


Fig. 9-1

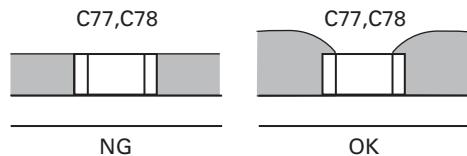


Fig. 9-2

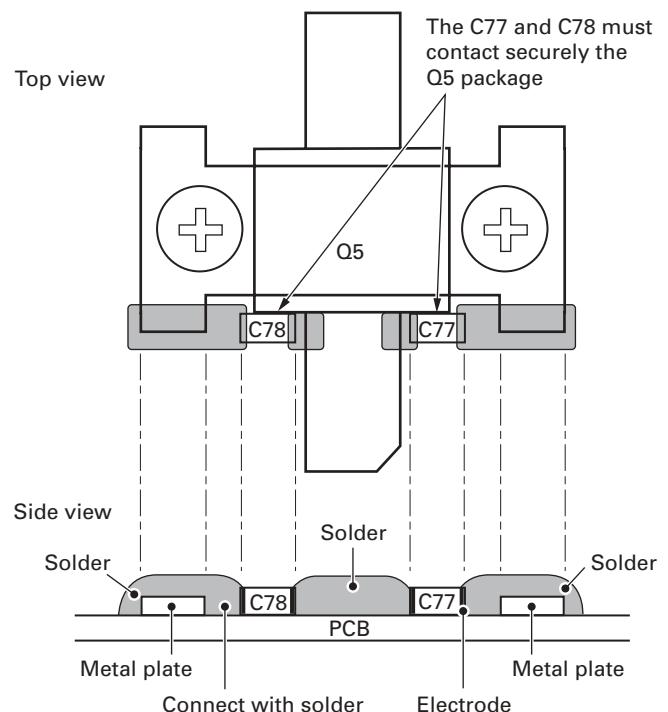


Fig. 9-3

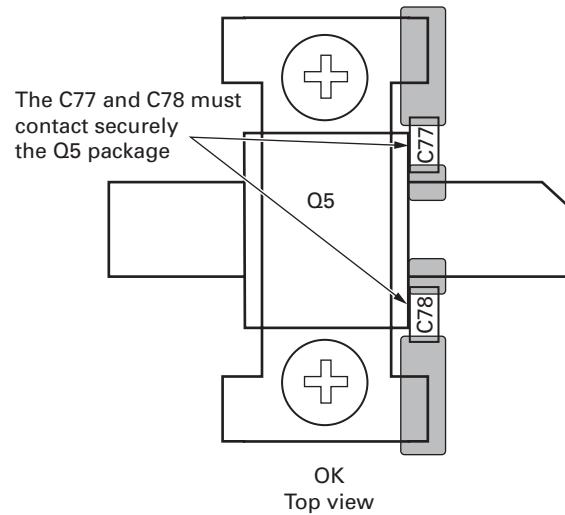


Fig. 9-4

CIRCUIT DESCRIPTION

1. Outline

The TK-8150 is a UHF/FM transceiver designed to operate in the frequency range of 450 to 500MHz. Transmission output power is 45 watts. The maximum channel capacity is 128.

The unit consists of receiver, transmitter, phase-locked loop (PLL) frequency synthesizer, and control circuits.

2. Receiver Circuit

The receiver is double conversion superheterodyne, designed to operate in the frequency range of 450MHz to 500MHz.

The receiver circuit consists of the following : 2-1 front-end circuit, 2-2 first mixer, 2-3 IF amplifier circuit, 2-4 audio amplifier circuit, and 2-5 squelch circuit.

2-1. Front-end Circuit

The front-end circuit consists of former BPF (D211 and D212), RF amplifier Q214, and latter BPF (D207, D208, D209 and D210). The BPF covers frequency ranges 450 to 500MHz.

The latter BPF (D207, D208, D209 and D210) attenuates the unwanted signals, and sends only the necessary signal to the first mixer.

2-2. First Mixer

The signal from the BPF is heterodyned with the first local oscillator signal from the PLL frequency synthesizer circuit at the first mixer DBM (Q210, Q211, Q212 and Q213) to become a 73.05MHz first intermediate frequency (IF) signal. The first IF signal is fed through two monolithic crystal filters (XF201; Wide, XF202; Narrow) to further remove spurious signals.

2-3. IF Amplifier

The first IF signal is amplified by Q207 and Q206, and then enters IC201 (FM system IC). The signal is heterodyned again with a second local oscillator signal (73.505MHz) with in IC201 to become a 455kHz second IF signal. The second IF signal is fed through a 455kHz ceramic filters (CF201 and CF203; Wide, CF202 and CF204; Narrow) to further eliminate unwanted signal, and the quadrature detection circuit FM-detects the signal to produce a base-band signal and output it from pin 9.

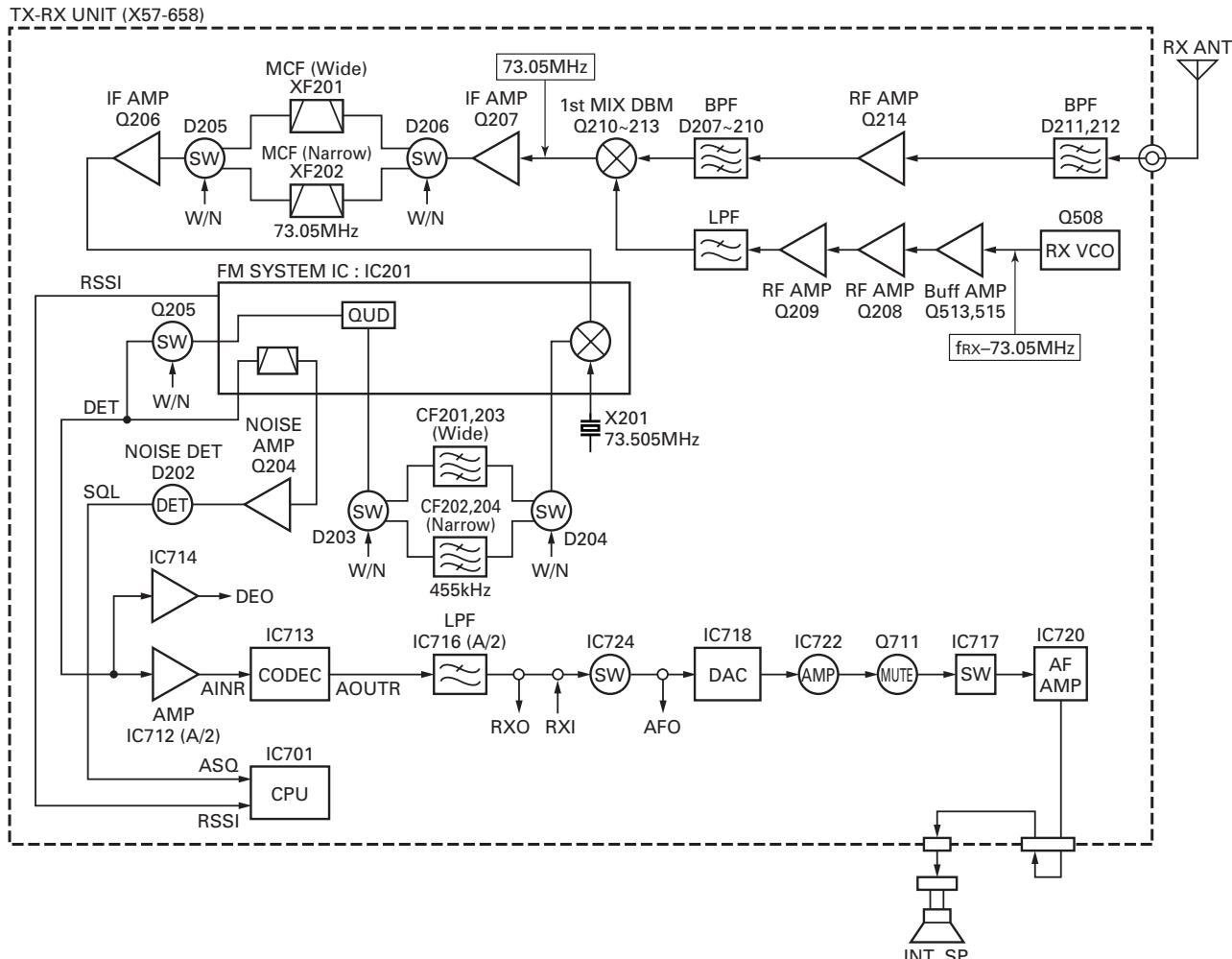


Fig. 1 Receiver circuit

CIRCUIT DESCRIPTION

2-4. Audio Amplifier

The recovered audio signal obtained from IC201 is amplified and anti-aliasing filtered by IC712 (A/2), inputted to the AINR terminal of CODEC IC (IC713), and audio processed by DSP (IC710). The processed audio signal from AOOUTR terminal of IC713 is amplified and filtered by IC716 (A/2) to a sufficient level. The audio signal goes to an electronic volume (IC718) and then it passes through the amplifier (IC722), to the input of multiplexer IC (IC717), and is amplified to drive a loudspeaker by an audio power amplifier (IC720). The audio output can be provided to external 4Ω speaker through the 6-pin ACC connector "ES1, ES2" on the rear panel. Q711 is a mute switch.

2-5. Squelch Circuit

The output signal from IC201 enters FM IC again, then passed through a band-pass filter.

The noise component output from IC201 is amplified by Q204 and rectified by D202 to produce a DC Voltage corresponding to the noise level. The DC voltage is sent to the analog port of the CPU (IC701).

IC201 outputs a DC voltage (RSSI) corresponding to the input of the IF amplifier.

3. Transmitter Circuit

The transmitter circuit consists of the following circuits : 3-1 microphone circuit, 3-2 modulation level adjustment circuit, 3-3 driver and final power amplifier circuit, and 3-4 automatic power control circuit.

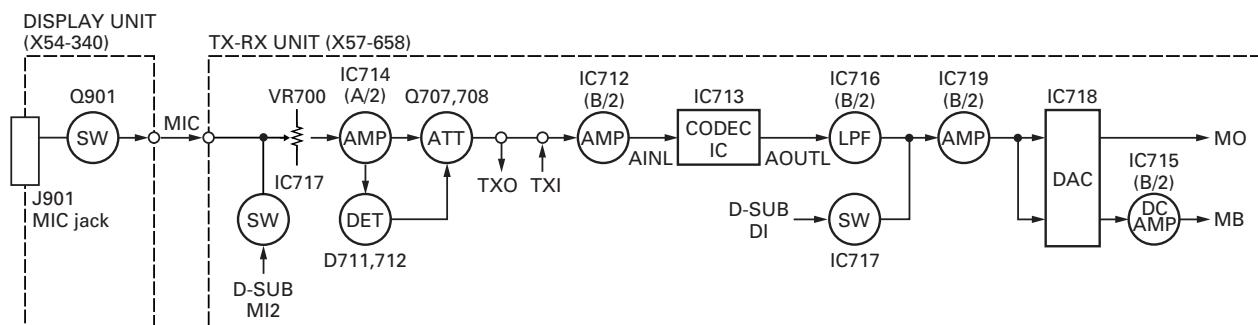


Fig. 2 Microphone circuit

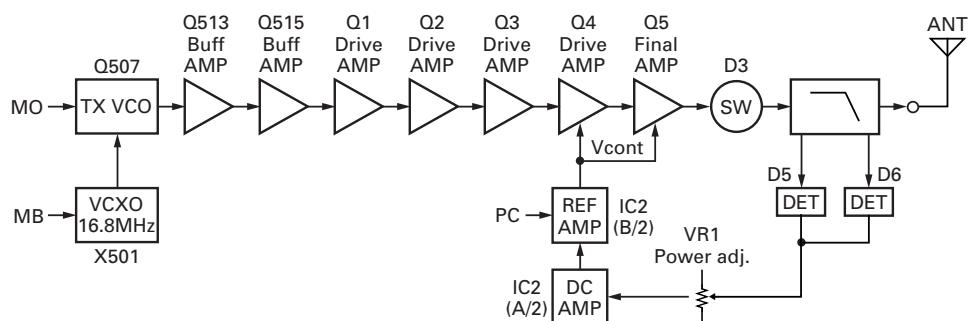


Fig. 3 Drive and final amplifier circuit

3-1. Microphone Circuit

The signal from the microphone passes through the mute switch (Q901) and is then routed to the Display unit (X54-340) and TX-RX unit (X57-658). The signal then passes through the AGC circuit to avoid signal distortion.

This circuit consists of IC714, D711, D712, Q707, and Q708. The AGC is operated by controlling the + and - side levels of amplitude using the current obtained by positive and negative detection of the amplified audio signal.

The audio signal is amplified by IC712 (B/2), input to the AINL terminal of CODEC IC (IC713), and audio processed by DSP (IC710). The processed audio signal from the AOOUTL terminal of IC713 is amplified and filtered by IC716 (B/2), and amplified by the summing amplifier IC719 (B/2).

3-2. Modulation Level Adjustment Circuit

The output of the summing amplifier IC719 (B/2) is passed to an electronic volume (IC718) for maximum deviation adjustment before being applied to a varactor diode in the voltage controlled oscillator VCO.

3-3. Driver and Final Power Amplifier Circuit

The transmit signal is generated by the TX VCO (Q507), amplified by Q513 and Q515. This amplified signal is amplified by Q1, Q2, Q3, Q4, and Q5. And this signal is passed to the FINAL stage. The RF power amplifier consists of MOS FET transistor.

CIRCUIT DESCRIPTION

3-4. Automatic Power Control, Circuit and Transmitter

The automatic power control (APC) circuit stabilizes the transmitter output power at a predetermined level, and consists of forward/reflected power detector circuits. The forward/reflected power detector circuits detects forward RF power and reflected RF power to DC voltage, and consists of a C coupling type detection circuit, RF detector D5/D6, and DC amplifier IC2 (A/2).

The voltage comparator (IC2 B/2) compares the voltage obtained by the above detected voltage with a reference voltage, set using the microprocessor and IC718 and IC715.

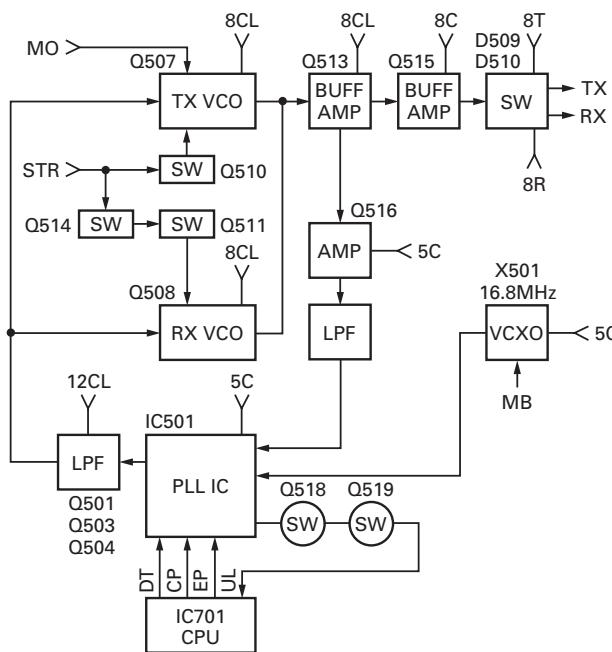
An APC voltage proportional to the difference between the sensed voltage and the reference voltage appears at the output of IC2. This output voltage controls the gate voltage for the drive amplifier Q4 and final amplifier Q5, which keeps the transmitter output power constant.

4. PLL Frequency Synthesizer

The transmit signal and the receive first L.O. signal are generated by the PLL digital frequency synthesizer. The frequency synthesizer consists of a transmitter voltage controlled oscillator (TX VCO; Q507), a receiver voltage controlled oscillator (RX VCO; Q508), a buffer amplifier (Q513 and Q515), an RF amplifier (Q516), a low-pass filter (Q501, Q503 and Q504), a PLL IC (IC501), and TX VCO/RX VCO switches (Q510 and Q511).

In the transmit signal mode, an operating frequency programming data is sent to IC501, from the CPU (IC701), to set the programmable counter within IC501. Q510 is turned on to activate the TX VCO and the output signal of the TX VCO is amplified by Q513 and Q515.

The signal is then divided down in frequency, at the programmable counter in IC501, to 5.0kHz or 6.25kHz which is compared in phase with a 5.0kHz or 6.25kHz reference signal, derived from 16.8MHz VCXO (X501) and a 1/3360 or a 1/

**Fig. 4 PLL circuit**

2688 fixed counter in IC501, at the phase comparator in IC501. The VCXO operates at 16.8MHz and its frequency stability is maintained within 2.5ppm (temperature range of -30 to +60 degrees).

The phase comparator output signal is fed into a low-pass filter (Q501, Q503 and Q504) before being applied to the TX vco as a frequency control voltage. This low-pass filter's power is supplied by the DC/DC converter (Q502, Q505, Q506, Q509, and Q512). If an unlock condition occurs in the phase locked loop, this condition is detected by Q518 and Q519. This causes the transmitter 8V supply cut off, resulting in the prevention of an unauthorized transmission.

The transmitter modulation signals (processed Mic. audio and sub-audible signalling) are applied to the TX VCO for frequency modulation.

In the receive mode, the VCO is substituted with Q508 (RX VCO) and it generates the receiver first local oscillator signal according to the data sent from the CPU (IC701). The basic operation of the synthesizer remains the same.

5. Control Circuit

The control circuit mainly consists of CPU, memory circuit, DSP circuit, and power supply circuit.

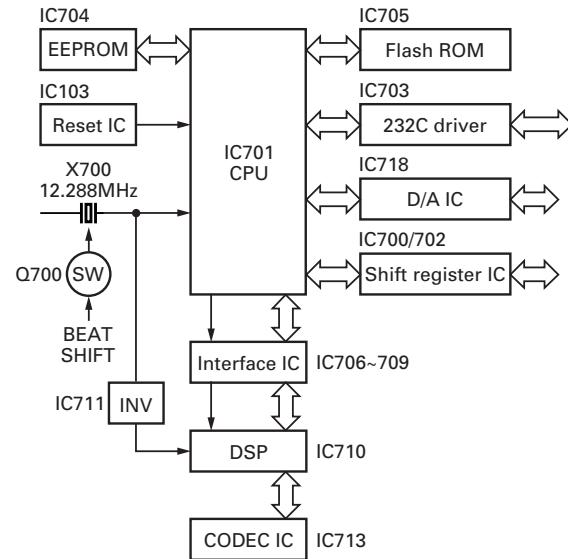
5-1. CPU

The CPU (IC701) controls the flash ROM (IC705), the DSP (IC710), the receiver circuit, the transmitter circuit, the control circuit, and the display circuit and transfers data to or from an external device.

5-2. Memory Circuit

IC705 has a flash ROM with a capacity of 4M bits that contains the control program for the CPU, the signal processing program for DSP and data such as channels and operating features.

This program can be easily written from an external devices. Data such as the operating status are programmed into the EEPROM (IC704).

**Fig. 5 Control circuit**

CIRCUIT DESCRIPTION

5-3. DSP Circuit

The DSP circuit filters transmit/receive audio signal and encode/decode signalling (QT, DQT, MSK, DTMF, 2-Tone, LTR ID). This circuit consists of IC710, IC706, IC707, IC708, IC709 and IC713.

The receive audio signal is converted from analog to digital by IC713 with a sampling frequency of 19.2kHz. The digitized audio signal is sent to DSP (IC710) to process the signalling signal and audio signal. The processed digital audio signal is fed to CODEC (IC713), converted from digital to analog, and the analog signal is output from pin 16 (AOUTR).

The transmit audio signal coming from IC714 (A/2) is amplified by IC712 (B/2), fed to pin 3 (AINL) of CODEC (IC713), and converted from analog to digital at a sampling frequency of 19.2kHz. The digitized transmit audio signal is AGC-processed, pre-emphasized and filtered at 300Hz to 3kHz by DSP (IC710), and the resulting signal is feed back to CODEC (IC713), and converted from digital to analog, and the analog signal is output from pin 15 (AOUTL).

IC706, IC707, IC708 and IC709 are interface IC between the CPU operated at 5.0V and the DSP operated at 3.3V.

5-4. Power Supply Circuit

The voltage is always applied from +B. D103 protects IC102 (DFF) against overvoltage.

Pulses from power switch are input to the IC102 clock line and the CPU (IC701). These clock pulses reverse the output High (radio : turn off) → Low (radio : turn on), do not reverse the output Low → High. The CPU (IC701) output the pulse to IC102 set line to reverse the output Low → High.

IGN is input to the IC102 reset line and the CPU (IC701). When the IC102 reset voltage rise, the output goes Low (ra-

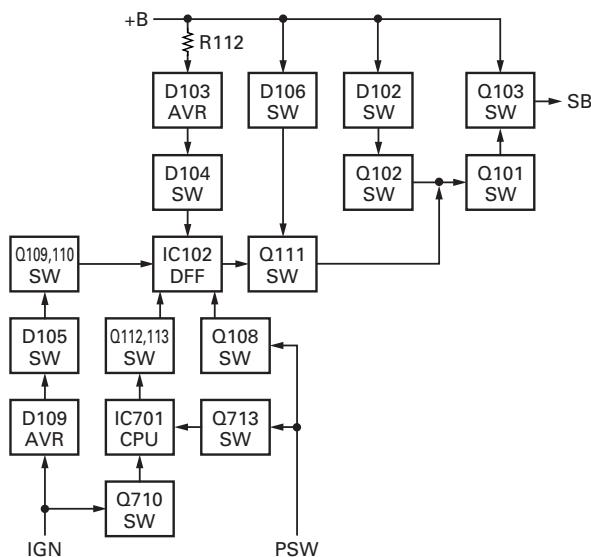


Fig. 6 Power supply circuit

dio : turn on). When IGN falls, the CPU (IC701) output the pulse to IC102 set line to reverse the output Low → High after the programmed time expire (Timed power off function).

If 24V is supplied to the radio by mistake, Q102 turns on, Q101 turns off, the power is forced to be turned off.

This circuit consists of IC104 (8V), IC105 (5V), IC106 (3.3V), IC108 (3.3V) and IC107 (1.8V).

6. Display Circuit

The display unit consists of CPU (IC904), LCD assembly, LED, and other components.

Channels are changed by the rotary switch (S1). The up/down signals from the rotary switch enter the CPU (IC904), and converted to a serial data signal, and are sent to the CPU (IC701) in the TX-RX unit. The on/off signals of keys other than the power switch, and the PTT and HOOK signals, are converted to serial data and sent to the CPU (IC701) in the TX-RX unit.

Data is displayed on the 12 digits and 3 digits dot matrix alphanumeric display.

7. RS-232C Circuit

The RS-232C circuit connects the RS-232C serial port of a personal computer directly to this model to perform FPU operation (Read and Write). The FPU operation can also be performed by connecting a programming cable (KPG-46) to the local microphone on the front panel. The 232C driver IC (IC703) changes the TTL-232C level. The firmware can only be rewritten with the local microphone on the front panel.

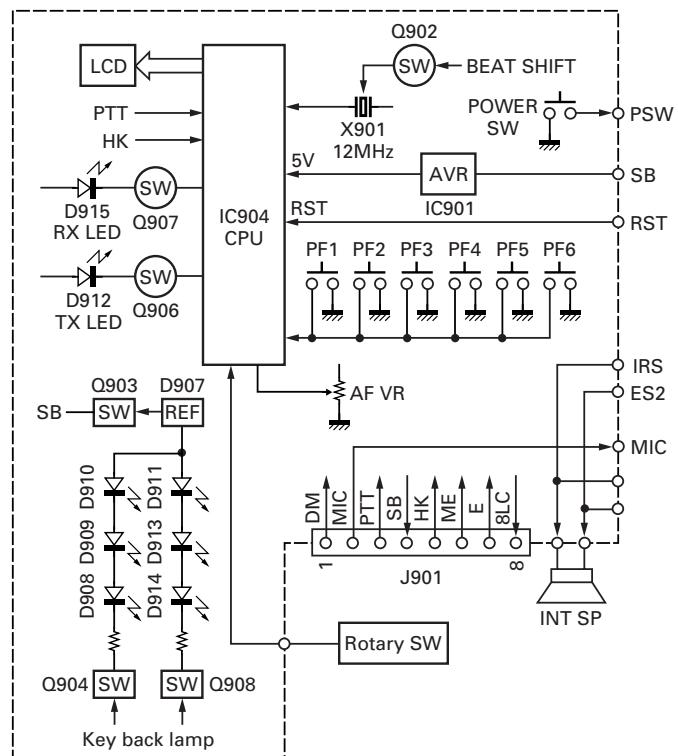


Fig. 7 Display circuit

SEMICONDUCTOR DATA

1. CPU : 30620M8A-5H1GP (TX-RX Unit IC701)

1-1. Pin Function

Pin No.	Name	I/O	Function
1		I/O	Go to port check mode L : Port check mode, H : User mode
2	WN	O	Wide/Narrow H : Narrow, L : Wide
3	8RC	O	8R Control H : 8R on, L : 8R off
4	INTx	O	INT Request to DSP L : INT request
5	STR	O	VCO TX/RX H : TX VCO, L : RX VCO
6	BYTE		+5V
7	CNVss		GND
8	8TC	O	8T Control H : 8T on, L : 8T off
9	SFT	O	Beat Shift H : SFT on, L : SFT off
10	RESET		CPU Reset
11	Xout		12.288MHz
12	Vss		GND
13	Xin		12.288MHz
14	Vcc		+5V
15	NMI	I	Not Used
16	IGN	I	Ignition Sense H : IGN. Sense off, L : IGN. Sense on
17	PSI	I	Power Down Det L : Power down det
18	LD	O	D/A Converter LD
19	CP	O	PLL IC Clock
20	EP	O	PLL IC Enable
21	CONTROL	O	Connect Codec TX Out to speaker line H : Connect, L : Disconnect
22	DT	O	Common Data
23	CK	O	Common Clock
24	SOE	O	Shift Register 1 OE
25	STB1	O	Shift Register 1 Strobe
26	AI4	I	AUX Input No.4 H : Inactive, L : Active
27	RXD2	I	Head Comm (RXD)
28	TXD2	O	Head Comm (TXD)
29	TXD1	O	Acc com2 (TXD)
30	RXD1	I	Acc com2 (RXD)
31	AI3	I	AUX Input No.3 H : Inactive, L : Active
32	AI2	I	AUX Input No.2 H : Inactive, L : Active
33	TXD0	O	Acc com1 (TXD)
34	RXD0	I	Acc com1 (RXD)
35	AI1	I	AUX Input No.1 H : Inactive, L : Active
36		O	Not Used
37	RDY	I	Hardware BUS Wait Control
38	ALE	O	Cannot Use (Address Latch)

Pin No.	Name	I/O	Function
39	HOLD	I	Cannot Use (BUS Hold)
40	HLDA	O	Cannot Use (BUS Hold Status)
41	BCLK	O	Cannot Use (Sys Clock Output)
42	RD	O	Flash ROM : RD, DSP : HDS1
43	BHE	O	Cannot Use (BUS High Enable)
44	WR	O	Flash ROM : WR, DSP : HDS2
45	SBC	O	SB Control H : SBC off, L : SBC on
46	AM	O	Audio Mute H : SP mute, L : SP unmute
47	HCS	O	DSP HCS
48	CS0	O	Flash CS
49~59	A19~A9	O	Cannot Use (Flash ROM Address BUS)
60	Vcc		+5V
61	A8	O	Cannot Use (Flash ROM Address BUS)
62	Vss		GND
63~70	A7~A0	O	Cannot Use (Flash ROM Address BUS)
71	PTO	O	Scrambler TX/RX Control H : RX, L : TX
72	PSW	I	Pow. Switch Det H : Inactive, L : Active
73	Inty	I	INT Request from DSP L : INT request
74	RS	O	DSP Reset H : DSP reset
75	SCL	O	EEPROM Clock
76	SDA	I/O	EEPROM Data
77	KEY	I	TX Request from ANI board L : TX request
78	STB2	O	Shift Register 2 Strobe
79~86	D7~D0		Cannot Use (Flash ROM Data BUS)
87	PDET	I	Power Det Level
88	CDET	I	Power Current Det Level
89	TEMP1	I	Power Temp1 Level
90	TEMP2	I	Power Temp2 Level
91	ASQ	I	Analog Squelch Level
92	RSSI	I	RSSI Level
93	UL	I	Unlock Level
94	Avss		GND
95		I	Not Used
96	Vref		+5V
97	Avcc		+5V
98	AUX	I	Emergency Channel Request from ANI board L : Channel request
99	INH	I	Audio Inhibit from ANI board H : SP unmute, L : SP mute
100	TCNT	I	Tone Control from ANI board L : Board tone output request

SEMICONDUCTOR DATA

2. Shift Register 1 : BU4094BCFV (TX-RX Unit IC702)

2-1. Pin Function

Pin No.	Port Name	Name	I/O	Function
1	STROBE	STB1	I	From CPU STB1 (Pin No.25)
2	SERIAL IN	DATA	I	From CPU Common Data (Pin No.22)
3	CLOCK	CLK	I	From CPU Common Clock (Pin No.23)
4	Q1	MI2	O	External Mic Mute L : Mute, H : Unmute
5	Q2	DI	O	External Data Line Mute L : Mute, H : Unmute
6	Q3	SP	O	Front SP/EXT SP Change L : Front SP, H : External SP
7	Q4	HR	O	Horn Alert L : Relay off, H : Relay on
8	VSS	VSS		GND
9	Qs	Qs	O	No Connect
10	Qs'	Qs'	O	No Connect
11	Q8	AO4	O	AUX Output No.4 H : AUX Out No.4 on, L : AUX Out No.4 off
12	Q7	AO3	O	AUX Output No.3 H : AUX Out No.3 on, L : AUX Out No.3 off
13	Q6	AO2	O	AUX Output No.2 H : AUX Out No.2 on, L : AUX Out No.2 off
14	Q5	AO1	O	AUX Output No.1 H : AUX Out No.1 on, L : AUX Out No.1 off
15	OUTPUT ENABLE	SOE	I	From CPU SOE (Pin No.24) L → Inactive, H → Active
16	VDD	VDD		+5V

3. Shift Register 2 : BU4094BCFV (TX-RX Unit IC700)

3-1. Pin Function

Pin No.	Port Name	Name	I/O	Function
1	STROBE	STB2	I	From CPU STB1 (Pin No.21)
2	SERIAL IN	DATA	I	From CPU Common Data (Pin No.22)
3	CLOCK	CLK	I	From CPU Common Clock (Pin No.23)
4	Q1	BC1	O	Scrambler Code 1 (For SCR) L : SCR Code1 off, H : SCR Code1 on
5	Q2	BC2	O	Scrambler Code 2 (For SCR) L : SCR Code2 off, H : SCR Code2 on
6	Q3	BC3	O	Scrambler Code 3 (For SCR) L : SCR Code3 off, H : SCR Code3 on
7	Q4	BC4	O	Scrambler Code 4 (For SCR) L : SCR Code4 off, H : SCR Code4 on
8	VSS	VSS		GND

Pin No.	Port Name	Name	I/O	Function
9	Qs	Qs	O	No Connect
10	Qs'	Qs'	O	No Connect
11	Q8	AC	O	Scrambler ON/OFF (For SCR) L : Scrambler on, H : Scrambler off
12	Q7	BUSY	O	Channel Busy (For ANI) L : Busy, H : Not Busy
13	Q6	EMG	O	Emergency ON/OFF (For ANI) L : Emergency on, H : Emergency off
14	Q5	PTO	O	PTT Output (For ANI) L : PTT on, H : PTT off
15	OUTPUT ENABLE	SOE		GND
16	VDD	VDD		+5V

4. D/A Converter : M62364FP (TX-RX Unit IC718)

4-1. Pin Function

Pin No.	Name	I/O	Function
1	Vin1	I	AF Volume First IN
2	Vout1	O	AF Volume First OUT
3	Vout2	O	AF Volume Second OUT
4	Vin2	I	AF Volume Second IN (From DAC Pin No.2)
5	VDD		+5V
6	LD	O	From CPU LD(Pin No.18)
7	CK	O	From CPU Common Clock (Pin No.23)
8	DT	O	From CPU Common Data (Pin No.22)
9	Vin3	I	GND
10	Vout3	O	Tune Varicap
11	Vout4	O	Side Tone Out
12	Vin4	I	Side Tone (From Option Board)
13	Vin5	I	
14	Vout5	O	Max Deviation
15	Vout6	O	
16	Vin6	I	DQT Balance
17	Do	O	Open
18	VDeref	I	
19	Reset	I	+5V
20	GND		GND
21	Vin7	I	+5V
22	Vout7	O	Frequency Adjust
23	Vout8	O	Auto Power Control
24	Vin8	I	+5V

SEMICONDUCTOR DATA

5. Display CPU : 30622M4A-443GP (Display Unit IC904)

5-1. Pin Function

Pin No.	Name	I/O	Function
1	DISP	O	LCD Display ON/OFF L : On, H : Off
2~5		O	Not used
6	BYTE		GND
7	CNVss		GND
8		O	Not used
9	SFT	O	Beat Shift L : Shift off, H : Shift on
10	RESET		CPU Reset
11	Xout		12.0MHz
12	Vss		GND
13	Xin		12.0MHz
14	Vcc		+5V
15	NMI	I	Not used
16~19		O	Not used
20	MIC	O	Mic Mute L : Mute, H : Unmute
21,22		O	Not used
23	DM	I/O	Mic Keypad Data
24		O	Not used
25	BLC	O	Mic Keypad Backlight Control L : Off, H : On
26		O	Not used
27	RXD2	I	Body Comm (RXD)
28	TXD2	O	Body Comm (TXD)
29	TXD1	O	PC Comm (TXD)/connect to PTT
30	RXD1	I	Open (RXD)
31		I	Not used
32		O	Not used
33	TXD0	O	Open (TXD)
34	RXD0	I	PC Comm (RXD)/connect to HOOK
35	HOOK	I	HOOK/connect to RXD0
36	PTT	I	PTT/connect to TXD1
37		O	Not used
38	BCK1	O	PF Key Backlight Control L : Off, H : On
39	PF1	I	PF1 Key Input
40	PF2	I	PF2 Key Input

Pin No.	Name	I/O	Function
41	PF3	I	PF3 Key Input
42	PF4	I	PF4 Key Input
43	PF5	I	PF5 Key Input
44	PF6	I	PF6 Key Input
45~49		O	Not used
50	BCK2		PF Key Backlight Control L : Off, H : On
51	TP3	I	Head Check Mode L : Head check mode, H : Not go to head check mode
52~56		O	Not used
57	BUSY	O	BUSY LED L : Off, H : On
58	TX	O	TX LED L : Off, H : On
59		O	Not used
60	Vcc		+5V
61		O	Not used
62	Vss		GND
63~66		O	Not used
67	RSW4	I	Rotary SW4
68	RSW3	I	Rotary SW3
69	RSW2	I	Rotary SW2
70	RSW1	I	Rotary SW1
71~81		O	Not used
82	LEDK	O	LCD Back Light Control L : Off, H : On
83	DO	O	LCD Data Output
84	DI	I	LCD Data Input
85	CL	O	LCD Clock
86	CE	O	LCD CS
87~92		O	Not used
93	VOL	I	Audio Volume Input
94	AVss		GND
95		O	Not used
96	AVref		+5V
97	AVcc		+5V
98~100		O	Not used

SEMICONDUCTOR DATA

6. DSP : 320VC5402PGE (TX-RX Unit IC710)

6-1. Pin Function

Pin No.	Name	I/O	Function
1,2,12,15, 35~38, 71~74,80, 90,110,126, 143,144	NC1~NC18	-	Not used (No connect)
6,58,69,81, 95,120,124, 135	HDO~HD7	I/O	HPI data bus
5,7~11,105, 107~109, 131~134, 136~141,	A0~A19	O	Not used (No connect)
13	HAS	I	HPI address strobe (Pull up)
17	HCS	I	HPI chip select
18	HR/W	I	HPI read/write
19	READY	I	Data ready (Pull up)
20	PS	O	Not used (No connect)
21	DS	O	Not used (No connect)
22	IS	O	Not used (No connect)
23	R/W	O	Not used (No connect)
24	MSTRB	O	Not used (No connect)
25	IOSTRB	O	Not used (No connect)
26	MSC	O	Not used (No connect)
27	XF	O	CODEC control (H : power down, L : active)
28	HOLDA	-	Not used (No connect)
29	IAQ	-	Not used (No connect)
30	HOLD	I	Hold (Pull up)
31	BIO	I	Serial data synchronize input
32	MP/MC	I	Not used (Pull down)
39	HCNTL0	I	HPI control 0
41	BCLKR0	I	Receive clock output (SCLK : 614.4kHz)
42	BCLKR1	-	Master clock output (MCLK : 4.9152MHz)
43	BFSR0	I	Frame sync. for receiver output (LRCK : 19.2kHz)
44	BFSR1	I	Not used (No connect)
45	BDR0	I	Serial data receive input
46	HCNTL1	I	HPI control 1
47	BDR1	-	Not used (No connect)
48	BCLKX0	I	Transmit clock input (SCLK : 614.4kHz)
49	BCLKX1	O	Not used (No connect)
51	HINT/TOUT1	O	Interrupt for Host CPU/Boot mode select (Pull up)

Pin No.	Name	I/O	Function
53	BFSX0	I	Frame sync. for transmitter input (LRCK : 19.2kHz)
54	BFSX1	I	Not used (No connect)
55	HRDY	-	Not used (No connect)
59	BDX0	O	Serial data transmit output
60	BDX1	-	Not used (No connect)
61	IACK	-	Not used (No connect)
62	HBIL	I	Byte identification (HPI)
63	NMI	I	Not used (Pull up)
64	INT0	I	Command interrupt from Host CPU
65	INT1	I	Not used (Pull up)
66	INT2	I	Boot mode select (Pull up)
67	INT3	I	Not used (Pull up)
77	CLKMD1	I	Clock mode select (Pull down)
78	CLKMD2	I	Clock mode select (Pull up)
79	CLKMD3	I	Clock mode select (Pull down)
82	TOUT0	-	Not used (No connect)
83	EMU0	I/O	Emulator 0 (to JTAG connector)
84	EMU1/OFF	I/O	Emulator 1 (to JTAG connector)
85	TDO	O	Test data output (to JTAG connector)
86	TDI	I	Test data input (to JTAG connector)
87	TRST	I	Test reset (to JTAG connector)
88	TCK	I	Test clock (to JTAG connector)
89	TMS	I	Test mode select (to JTAG connector)
92	HPIENA	I	Not used (Pull up)
94	CLKOUT	O	Not used (No connect)
96	X1	-	12.288MHz (System clock)
97	X2/CLKIN	-	12.288MHz (System clock)
98	RS	I	DSP reset input
99~104, 113~119, 121~123	D0~D15	-	Not used (No connect)
127	HDS1	I	HPI data strobe1 (Pull up)
129	HDS2	I	HPI data strobe2 (Pull down)
3,14,34,40, 50,57,70, 76,93,106, 111,128	Vss	-	GND
4,33,56, 75,112,130	DVDD	-	VDD for I/O pins (+3.3V)
16,52,68, 91,125,142	CVDD	-	VDD for core CPU (+1.8V)

COMPONENTS DESCRIPTION**1. Display Unit (X54-3400-10)**

Ref. No.	Part Name	Description
IC901	IC	Voltage regulator
IC902	IC	Voltage detector
IC904	IC	CPU
Q901	FET	MIC switch
Q902	FET	DC switch
Q903	Transistor	Current driver
Q904~908	Transistor	DC switch
Q909,910	Transistor	Inverter
Q911	Transistor	DC switch
D902~906	Diode	Surge protector
D907	Zener diode	Voltage reference
D908~911	LED	Key backlight
D912	LED	TX light
D913,914	LED	Key backlight
D915	LED	RX light
D916	Zener diode	Surge protector
D917	Varistor	Current protector
D918	Zener diode	Surge protector

2. TX-RX Unit (X57-6580-10)

Ref. No.	Part Name	Description
IC2	IC	DC amplifier
IC101	IC	Voltage detector
IC102	IC	D flip-flop
IC103	IC	Voltage detector
IC104~108	IC	Voltage regulator
IC201	IC	FM IF system
IC202	IC	Multiplexer
IC501	IC	PLL
IC700	IC	Shift register
IC701	IC	CPU
IC702	IC	Shift register
IC703	IC	RS232C transceiver
IC704	IC	EEPROM
IC705	IC	Flash ROM
IC706	IC	Bus transceiver
IC707~709	IC	OR gate
IC710	IC	DSP
IC711	IC	Inverter

Ref. No.	Part Name	Description
IC712	IC	AF amplifier
IC713	IC	CODEC
IC714	IC	MIC amplifier/AGC
IC715	IC	Buffer amplifier
IC716	IC	Anti-aliasing filter
IC717	IC	Multiplexer
IC718	IC	D/A converter
IC719	IC	AF amplifier
IC720	IC	Audio power amplifier
IC721	IC	DC amplifier
IC722	IC	AF amplifier
IC723	IC	Voltage regulator
IC724	IC	Multiplexer
IC725	IC	Voltage detector
Q1,2	Transistor	RF amplifier
Q3	FET	RF amplifier
Q4	FET	Drive amplifier
Q5	FET	Final amplifier
Q101	Transistor	DC switch
Q102	Transistor	Switch
Q103	FET	DC switch
Q104~113	Transistor	DC switch
Q201~203	Transistor	W/N switch
Q204	Transistor	Noise amplifier
Q205	Transistor	W/N switch
Q206	Transistor	IF amplifier
Q207	Transistor	Pre IF amplifier
Q208,209	Transistor	RF amplifier
Q210~213	FET	Mixer
Q214	Transistor	RF amplifier
Q501	Transistor	Active filter
Q502	FET	Oscillator
Q503,504	Transistor	Active filter
Q505,506	Transistor	DC switch
Q507	FET	TX oscillator
Q508	FET	RX oscillator
Q509	Transistor	AVR
Q510,511	Transistor	TX/RX switch
Q512	Transistor	Ripple filter
Q513	Transistor	Buffer amplifier

COMPONENTS DESCRIPTION

Ref. No.	Part Name	Description
Q514	Transistor	Inverter
Q515	Transistor	Buffer amplifier
Q516	Transistor	Amplifier
Q517	Transistor	Ripple filter
Q518~520	Transistor	DC switch
Q521	Transistor	Inverter
Q700	FET	DC switch
Q701~706	Transistor	DC switch
Q707,708	Transistor	MIC gain switch
Q709	Transistor	Inverter
Q710,711	Transistor	DC switch
Q712,713	FET	DC switch
Q714	Transistor	DC switch
Q716	Transistor	DC switch
Q719,720	Transistor	DC switch
D1	Zener diode	Surge protector
D2	Zener diode	Voltage protection
D3,4	Diode	ANT switch
D5,6	Diode	RF detector
D9	Diode	ANT switch
D51	Diode	Surge protector
D101	Diode	Protection of reverse connection
D102,103	Zener diode	Voltage reference
D104~106	Diode	DC switch

Ref. No.	Part Name	Description
D108	Diode	Surge protector
D109	Zener diode	Voltage reference
D201	Diode	DC switch
D202	Diode	Noise detection
D203~206	Diode	Wide/Narrow switch
D207~212	Varicap	BPF tuning
D501~504	Varicap	Frequency control
D505	Diode	Voltage doubler
D506	Varicap	Modulator
D507	Diode	Voltage doubler
D508	Zener diode	Voltage reference
D509,510	Diode	RF switch
D515	Diode	DC switch
D700~703	Diode	Surge protector
D704	Diode	Voltage reduction
D705~709	Diode	Surge protector
D710	Zener diode	Surge protector
D711,712	Diode	AF detector
D714	Diode	DC switch
D715	Zener diode	Surge protector
D717,718	Zener diode	Surge protector
D785~788	Zener diode	Surge protector
D789	Varistor	Current protector
D791	Varistor	Current protector

TK-8150

PARTS LIST

CAPACITORS

CC 45 TH 1H 220 J
 1 2 3 4 5 6

1 = Type ... ceramic, electrolytic, etc.

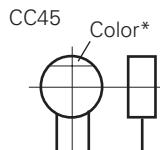
2 = Shape ... round, square, etc.

3 = Temp. coefficient

4 = Voltage rating

5 = Value

6 = Tolerance



• Capacitor value

010 = 1pF

100 = 10pF

101 = 100pF

102 = 1000pF = 0.001μF

103 = 0.01μF

2 2 0 = 22pF

Multiplier

2nd number

1st number

• Temperature coefficient

1st Word	C	L	P	R	S	T	U
Color*	Black	Red	Orange	Yellow	Green	Blue	Violet
ppm/°C	0	-80	-150	-220	-330	-470	-750

2nd Word	G	H	J	K	L
ppm/°C	±30	±60	±120	±250	±500

Example : CC45TH = -470±60ppm/°C

• Tolerance (More than 10pF)

Code	C	D	G	J	K	M	X	Z	P	No code
(%)	±0.25	±0.5	±2	±5	±10	±20	+40	+80	+100	More than 10μF : -10~+50

(Less than 10pF)

Code	B	C	D	F	G
(pF)	±0.1	±0.25	±0.5	±1	±2

• Voltage rating

1st word \ 2nd word	A	B	C	D	E	F	G	H	J	K	V
0	1.0	1.25	1.6	2.0	2.5	3.15	4.0	5.0	6.3	8.0	-
1	10	12.5	16	20	25	31.5	40	50	63	80	35
2	100	125	160	200	250	315	400	500	630	800	-
3	1000	1250	1600	2000	2500	2150	4000	5000	6300	8000	-

CHIP CAPACITORS

(EX) CC 73 F SL 1H 000 J
 1 2 3 4 5 6 7

Refer to the table above.

1 = Type

2 = Shape

3 = Dimension

4 = Temp. coefficient

5 = Voltage rating

6 = Value

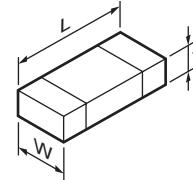
7 = Tolerance

(Chip) (CH, RH, UJ, SL)

(EX) CK 73 F F 1H 000 Z
 1 2 3 4 5 6 7

(Chip) (B, F)

• Dimension



Chip capacitor

Code	L	W	T
Empty	5.6±0.5	5.0±0.5	Less than 2.0
A	4.5±0.5	3.2±0.4	Less than 2.0
B	4.5±0.5	2.0±0.3	Less than 2.0
C	4.5±0.5	1.25±0.2	Less than 1.25
D	3.2±0.4	2.5±0.3	Less than 1.5
E	3.2±0.2	1.6±0.2	Less than 1.25
F	2.0±0.3	1.25±0.2	Less than 1.25
G	1.6±0.2	0.8±0.2	Less than 1.0
H	1.0±0.05	0.5±0.05	0.5±0.05

Chip resistor

Code	L	W	T
E	3.2±0.2	1.6±0.2	1.0
F	2.0±0.3	1.25±0.2	1.0
G	1.6±0.2	0.8±0.2	0.5±0.1
H	1.0±0.05	0.5±0.05	0.35±0.05

• Rating wattage

Code	Wattage	Code	Wattage	Code	Wattage
1J	1/16W	2C	1/6W	3A	1W
2A	1/10W	2E	1/4W	3D	2W
2B	1/8W	2H	1/2W		

1 = Type ... ceramic, electrolytic, etc.
 2 = Shape ... round, square, etc.
 3 = Dimension
 4 = Temp. coefficient
 5 = Voltage rating
 6 = Value
 7 = Tolerance

PARTS LIST

* New Parts. Δ indicates safety critical components.

Parts without **Parts No.** are not supplied.Les articles non mentionnés dans le **Parts No.** ne sont pas fournis.Teile ohne **Parts No.** werden nicht geliefert.

L : Scandinavia
Y : PX (Far East, Hawaii)
Y : AAFES (Europe)

K : USA
T : England
X : Australia

P : Canada
E : Europe
M : Other Areas

TK-8150 (Y51-4920-10)
 DISPLAY UNIT (X54-3400-10)

Ref. No.	Address	New parts	Parts No.	Description	Desti-nation
TK-8150					
1	1B	*	A01-2185-12	CABINET	
2	3A	*	A62-1037-03	PANEL ASSY	
4	1C	*	B11-1297-04	NAME PLATE	
5	1D	*	B62-1619-00	INSTRUCTION MANUAL	
6	3A	*	B72-2087-04	MODEL NAME PLATE	
8	2A		E04-0167-05	RF COAXIAL PECEPTACLE (M)	
9	1C	*	E30-3478-05	DC CORD ASSY	
-			E37-0808-05	LEAD WIRE WITH CONNECTOR	
11	3A	*	E37-1002-15	LEAD WIRE WITH CONNECTOR (SP)	
12	1A	*	E37-1003-05	LEAD WIRE WITH CONNECTOR (GP/ACC)	
13	3A	*	E37-1004-05	LEAD WIRE WITH CONNECTOR (TXRX-CONT)	
14	1B,2C	*	E37-1031-05	LEAD WIRE WITH CONNECTOR (SHORT PLUG)	
15	1A	*	E37-1074-05	LEAD WIRE WITH CONNECTOR (DC CODE)	
-		*	E37-1075-05	LEAD WIRE WITH CONNECTOR	
18	2C		F05-1537-05	FUSE (BLADE) (15A/32V)	
19	2B		F09-0445-05	CAP	
20	2C	*	F29-0478-05	PROTECTIVE COVER	
22	2B		G02-0861-04	FLAT SPRING	
23	2B	*	G02-0894-04	EARTH SPRING	
24	2B	*	G02-0896-04	FLAT SPRING	
25	2B	*	G02-0900-04	EARTH SPRING	
26	2B	*	G02-1801-03	EARTH SPRING	
27	2B	*	G02-1802-04	EARTH SPRING	
28	1B	*	G53-1551-02	PACKING	
29	2A	*	G53-1552-03	PACKING	
30	1A	*	G53-1554-03	PACKING	
31	3A	*	G53-1555-04	PACKING	
33	3D	*	H52-1921-02	ITEM CARTON CASE	
35	2C		J19-1584-05	HOLDER (MIC)	
36	2B	*	J21-8437-14	HARDWARE FIXTURE	
37	1C	*	J29-0697-03	BRACKET	
38	2C		J61-0307-05	BAND	
40	3A	*	K29-9221-03	KNOB	
41	3A	*	K29-9222-02	KEY TOP	
A	1B	*	N09-2409-05	HEXAGON HEAD SCREW	
B	3A		N10-2070-46	HEXAGON NUT	
C	1A,2B,3B		N67-3008-46	PAN HEAD SEMS SCREW W	
D	3A		N80-2606-46	PAN HEAD TAPITTE SCREW	
E	2B,3B		N87-2606-46	BRAZIER HEAD TAPITTE SCREW	
F	1B	*	N87-2620-46	BRAZIER HEAD TAPITTE SCREW	
G	1B,2B	*	N87-3008-45	BRAZIER HEAD TAPITTE SCREW	
H	2C	*	N99-2033-05	SCREW SET	
43	3A	*	T07-0738-05	SPEAKER	
44	1C		T91-0621-15	MICROPHONE	
46	3A	*	X54-3400-10	DISPLAY UNIT	

Ref. No.	Address	New parts	Parts No.	Description	Desti-nation
DISPLAY UNIT (X54-3400-10)					
101	3A	*	B39-0875-05	LCD ASSY	
D908-911			B30-2254-05	LED	
D912			B30-2193-05	LED (RED)	
D913,914			B30-2254-05	LED	
D915			B30-2194-05	LED (GREEN)	
C901			CC73GCH1H101J	CHIP C 100PF J	
C902			CK73GB1H102K	CHIP C 1000PF K	
C903			CK73GB1H103K	CHIP C 0.010UF K	
C904			CK73GB1H102K	CHIP C 1000PF K	
C905,906			CC73GCH1H101J	CHIP C 100PF J	
C907			CK73GB1H102K	CHIP C 1000PF K	
C909,910			CC73GCH1H101J	CHIP C 100PF J	
C911,912			C92-0628-05	CHIP-TAN 10UF 10WV	
C914			CK73GB1H102K	CHIP C 1000PF K	
C915-917			CC73GCH1H101J	CHIP C 100PF J	
C918,919			CK73GB1H103K	CHIP C 0.010UF K	
C921			CK73GB1H103K	CHIP C 0.010UF K	
C922			CC73GCH1H060B	CHIP C 6.0PF B	
C923			CK73GB1C223K	CHIP C 0.022UF K	
C924			C92-0628-05	CHIP-TAN 10UF 10WV	
C925			CK73GB1H102K	CHIP C 1000PF K	
C926,927			CC73GCH1H270J	CHIP C 27PF J	
C928			CK73GB1H103K	CHIP C 0.010UF K	
C929,930			CK73GB1H102K	CHIP C 1000PF K	
C931			CK73GB1H103K	CHIP C 0.010UF K	
C932			CK73GB1H102K	CHIP C 1000PF K	
C933			CK73GB1H103K	CHIP C 0.010UF K	
C934,935			CK73GB1H102K	CHIP C 1000PF K	
C938,939			CK73GB1H102K	CHIP C 1000PF K	
C941,942			CK73GB1H102K	CHIP C 1000PF K	
C943			CK73GB1H103K	CHIP C 0.010UF K	
C944			CK73GB1H102K	CHIP C 1000PF K	
CN901			E40-5538-05	PIN ASSY	
CN902			E40-6140-05	PIN ASSY	
CN905			E40-5823-05	FLAT CABLE CONNECTOR	
CN906			E40-5752-05	PIN ASSY	
J901			E08-0877-05	MODULAR JACK	
W1	3A	*	E37-1057-05	LEAD WIRE WITH CONNECTOR	
L901,902			L40-1095-34	SMALL FIXED INDUCTOR (1UH)	
X901			L77-1863-05	CRYSTAL RESONATOR (12.0MHz)	
R902			RK73GB1J104J	CHIP R 100K J 1/16W	
R904,905			R92-1252-05	CHIP R 0 OHM J 1/16W	
R906,907			RK73GB1J102J	CHIP R 1.0K J 1/16W	
R908			RK73GB1J473J	CHIP R 47K J 1/16W	
R909			RK73GB1J472J	CHIP R 4.7K J 1/16W	
R910			RK73GB1J104J	CHIP R 100K J 1/16W	
R913			RK73GB1J473J	CHIP R 47K J 1/16W	
R914			RK73GB1J561J	CHIP R 560 J 1/16W	
R915			R92-0670-05	CHIP R 0 OHM	
R916-918			RK73GB1J473J	CHIP R 47K J 1/16W	
R919			RK73GB1J333J	CHIP R 33K J 1/16W	
R920			RK73FB2A560J	CHIP R 56 J 1/10W	

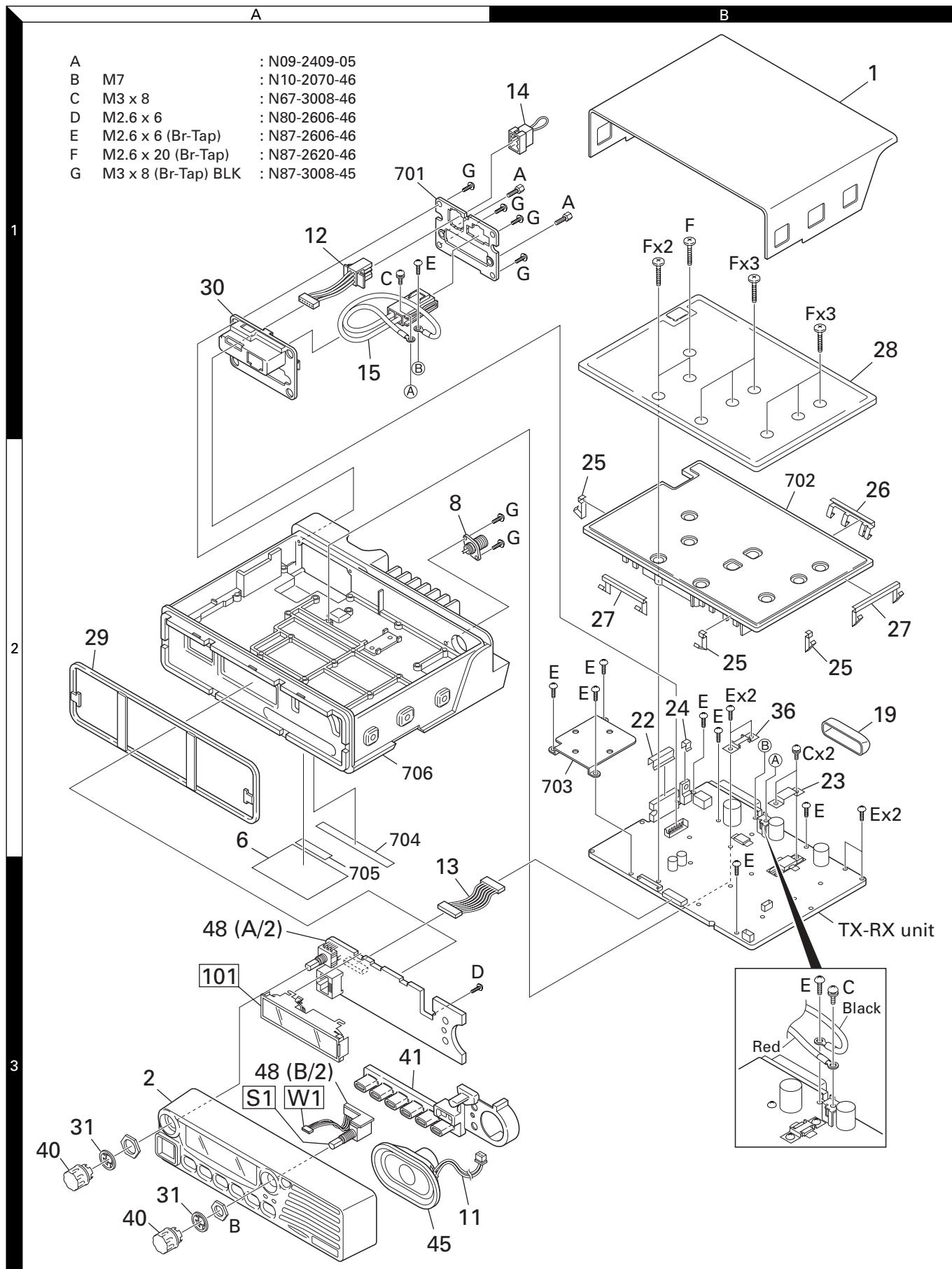
PARTS LIST

TX-RX UNIT (X57-6580-10)

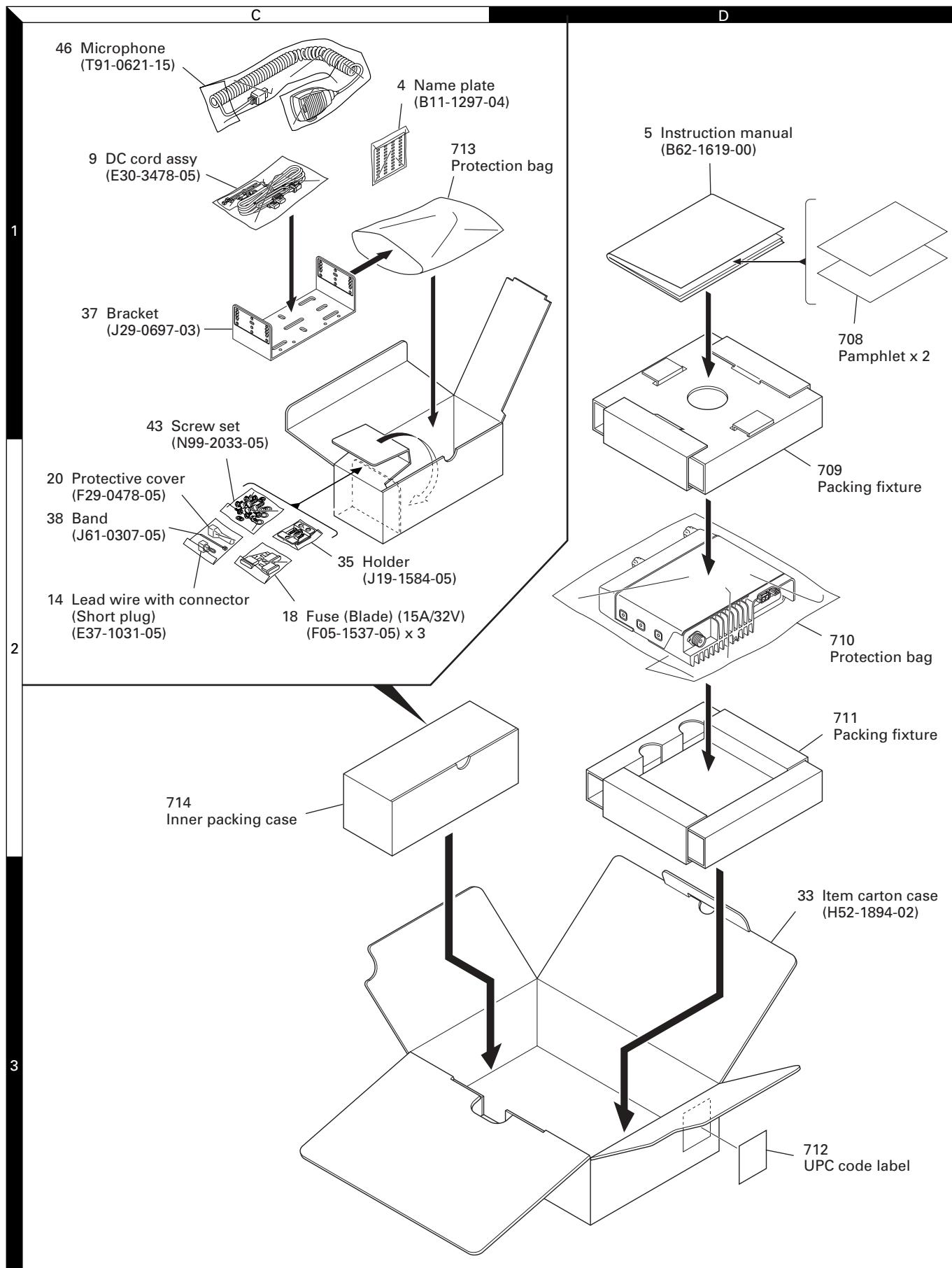
Ref. No.	Address	New parts	Parts No.	Description	Desti-nation	Ref. No.	Address	New parts	Parts No.	Description	Desti-nation
Q503,504			2SC4116(BL)	TRANSISTOR							
Q505			2SA1832(GR)	TRANSISTOR							
Q506			2SC4738(GR)	TRANSISTOR							
Q507,508			2SK508NV(K52)	FET							
Q509-512			2SC4116(GR)	TRANSISTOR							
Q513			2SC5108(Y)	TRANSISTOR							
Q514			DTC114EE	DIGITAL TRANSISTOR							
Q515,516			2SC5108(Y)	TRANSISTOR							
Q517			2SC4116(GR)	TRANSISTOR							
Q518			2SA1832(GR)	TRANSISTOR							
Q519			DTC144EE	DIGITAL TRANSISTOR							
Q520			2SC4116(GR)	TRANSISTOR							
Q521			DTC144EE	DIGITAL TRANSISTOR							
Q700			2SK1824	FET							
Q701			DTC114EE	DIGITAL TRANSISTOR							
Q702			DTC144EE	DIGITAL TRANSISTOR							
Q703-706			DTD114EK	DIGITAL TRANSISTOR							
Q707			2SC4116(Y)	TRANSISTOR							
Q708			2SA1586(Y,GR)	TRANSISTOR							
Q709			DTC144EE	DIGITAL TRANSISTOR							
Q710			DTC114EE	DIGITAL TRANSISTOR							
Q711			2SD1757K	TRANSISTOR							
Q712			2SJ243	FET							
Q713			2SK1824	FET							
Q714			DTC144EE	DIGITAL TRANSISTOR							
Q716			DTC114EE	DIGITAL TRANSISTOR							
Q719,720			DTC114YE	DIGITAL TRANSISTOR							
TH1			S1R104J475H	THERMISTOR							
TH201			157-503-53006	THERMISTOR							
TH202			157-104-65001	THERMISTOR							

TK-8150

EXPLODED VIEW



PACKING

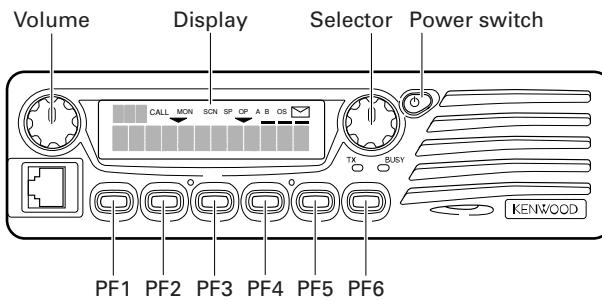


Parts with the exploded numbers larger than 700 are not supplied.

ADJUSTMENT

1. Key Functions

1-1. Front Panel



1-2. Panel Test Mode

■ When the Function is Off (▼ does not appear)

Controls	Function
PF1	Test channel down
PF2	Test channel up
PF3	Test mode/Tuning mode changeover
PF4	Noise squelch off
PF5	Wide/Narrow changeover When the narrow is on, □ appears
PF6	After pressing PF6 key, press PF3, PF4, PF5, and PF6 to activate the following functions (See the table below)
Selector knob	Signalling up/down

■ When the Function is On (▼ appears)

Controls	Function
PF1	No function
PF2	No function
PF3	— appears on the right side When the Sig No.9 (DTMF) is on, DTMF [9] is transmitted. When the Sig No.12 (MSK) is on, the continuous MSK tone is transmitted.
PF4	Compander on (OP icon appears) / off
PF5	Beat shift on (B icon appears) / off
PF6	MSK speed changeover (1200/2400bps). When MSK speed is 2400 bps, the Mail icon appears.
Selector knob	Test signalling up/down

1-3. Panel Tuning mode

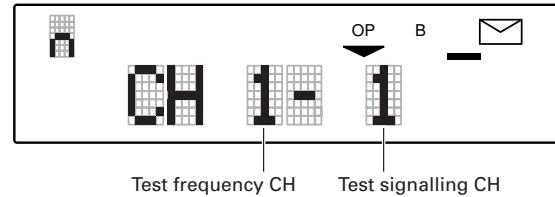
Controls	Function
PF1	Tuning item down
PF2	Tuning item up
PF3	Test mode/Tuning mode changeover
PF4	Noise squelch off
PF5	Wide/Narrow changeover
PF6	Tuning value write→Move to the next tuning item
Selector knob	Tuning value up/down

2. Panel Test Mode

The transceiver's transmission output, receiver sensitivity, and other items are measured and QT, DQT, 2-Tone and DTMF signalling is decoded in this mode.

2-1. To Enter the Panel Test Mode

Hold down [PF1] and turn the power switch on to enter this mode. The test frequency channel and test signalling channel will be displayed.



2-2. Test Frequency Channel (MHz)

CH	RX	TX
1/FC	475.15	475.00
2/FL	450.15	450.00
3/FH	499.85	500.00
4	462.55	462.50
5	487.55	487.50
6	475.00	475.00
7	475.20	475.20
8	475.40	475.40

2-3. Test Signalling Channel

No.	Decode	Encode
1	None	None
2	LTR	LTR
3	QT 67Hz	QT 67Hz
4	QT 151.4Hz	QT 151.4Hz
5	QT 210.7Hz	QT 210.7Hz
6	QT 254.1Hz	QT 254.1Hz
7	DQT 023N	DQT 023N
8	DQT 754I	DQT 754I
9	DTMF decode (159D)	DTMF encode (159D)
10	2-Tone decode A : 304.7Hz, B : 3106.0Hz	2-Tone encode A : 304.7Hz, B : 3106.0Hz
11	Single tone 979.9Hz	Single tone 979.9Hz
12	MSK	MSK
13	None	Single tone : 1kHz

3. Panel Tuning Mode

The transceiver is adjusted in this mode.

3-1. To Enter the Panel Tuning Mode

Press the [PF3] key in the panel test mode.

The adjustment items, the frequency and signalling, other than the maximum deviation and sensitivity, return to the values that were effective for the test frequency channel and test signalling channel before entering the panel tuning mode.

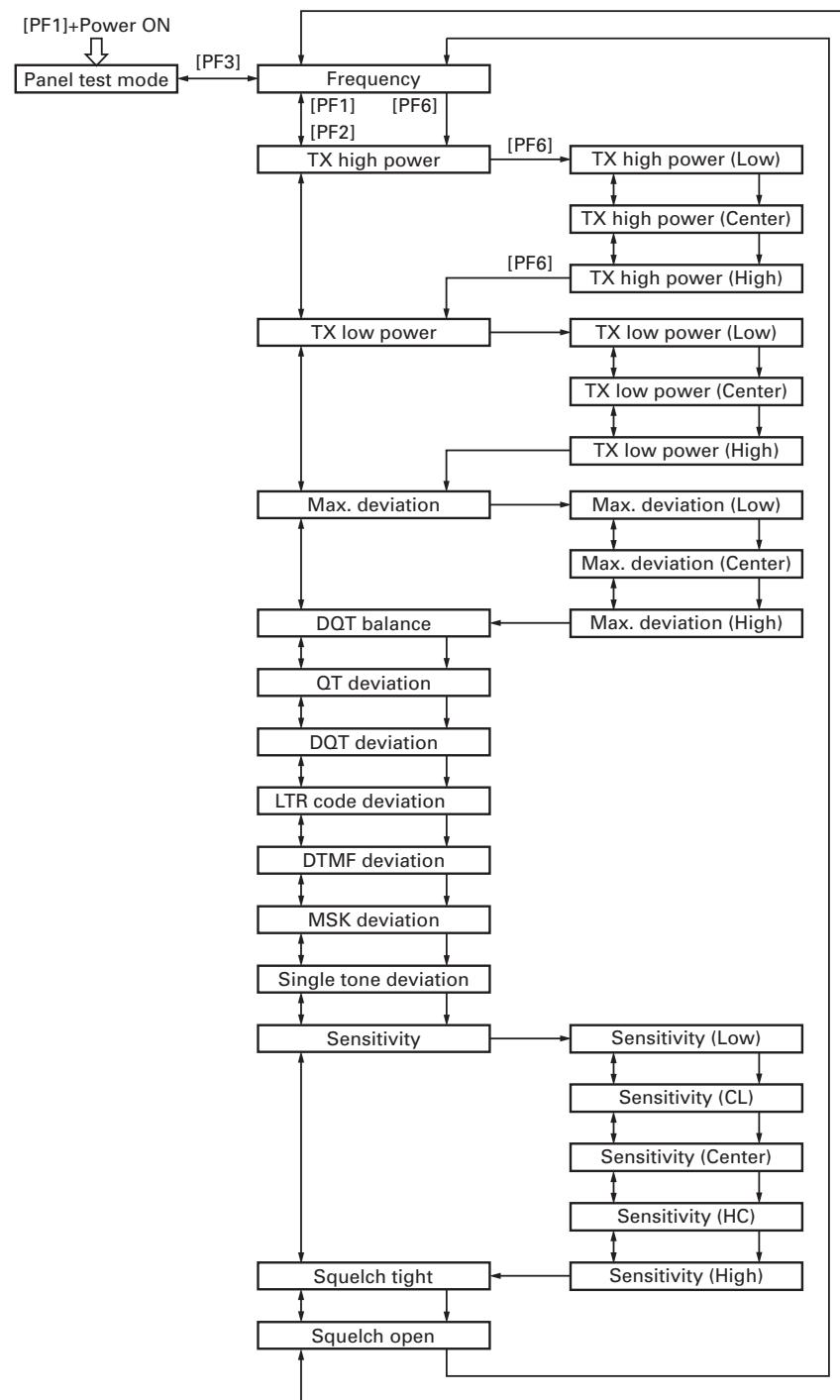
ADJUSTMENT**3-2. Tuning Item and Display (** : 1~256)**

Tuning item	W/N	LCD display	S-LCD	Signalling
Frequency	-	F r e q _ _ _ _ _ * * *		No signalling
TX high power (Low)	-	H i _ P o w e r _ * * *	_ _ L	No signalling
TX high power (Center)	-	H i _ P o w e r _ * * *	_ _ C	No signalling
TX high power (High)	-	H i _ P o w e r _ * * *	_ _ H	No signalling
TX low power (Low)	-	L o _ P o w e r _ * * *	_ _ L	No signalling
TX low power (Center)	-	L o _ P o w e r _ * * *	_ _ C	No signalling
TX low power (High)	-	L o _ P o w e r _ * * *	_ _ H	No signalling
Max. deviation (Low)	Wide	M a x _ D e v _ _ * * *	_ _ L	No signalling
Max. deviation (Center)	Wide	M a x _ D e v _ _ * * *	_ _ C	No signalling
Max. deviation (High)	Wide	M a x _ D e v _ _ * * *	_ _ H	No signalling
Max. deviation (Low)	Narrow	M a x _ D e v _ _ * * *	n _ L	No signalling
Max. deviation (Center)	Narrow	M a x _ D e v _ _ * * *	n _ C	No signalling
Max. deviation (High)	Narrow	M a x _ D e v _ _ * * *	n _ H	No signalling
DQT balance	Wide	D Q T _ B a l _ _ * * *		Square waveform 100Hz
DQT balance	Narrow	D Q T _ B a l _ _ * * *	n _ _	Square waveform 100Hz
QT deviation	Wide	Q T _ D e v _ _ _ * * *		QT : 67.0Hz
QT deviation	Narrow	Q T _ D e v _ _ _ * * *	n _ _	QT : 67.0Hz
DQT deviation	Wide	D Q T _ D e v _ _ * * *		DQT : 023N
DQT deviation	Narrow	D Q T _ D e v _ _ * * *	n _ _	DQT : 023N
LTR code deviation	Wide	L T R _ D e v _ _ * * *		LTR data : Area=0, Goto=12, Home=12, ID=47, Free=25
LTR code deviation	Narrow	L T R _ D e v _ _ * * *	n _ _	LTR data : Area=0, Goto=12, Home=12, ID=47, Free=25
DTMF deviation	Wide	D T M F _ D e v _ _ * * *		DTMF : 9
DTMF deviation	Narrow	D T M F _ D e v _ _ * * *	n _ _	DTMF : 9
MSK deviation	Wide	M S K _ D e v _ _ * * *		0xAAA...
MSK deviation	Narrow	M S K _ D e v _ _ * * *	n _ _	0xAAA...
Single tone deviation	Wide	T o n e _ D e v _ _ * * *		1000Hz
Single tone deviation	Narrow	T o n e _ D e v _ _ * * *	n _ _	1000Hz
Sensitivity	-	S e n s _ _ _ _ _ * * *	_ _ L	No signalling, Squelch off
Sensitivity	-	S e n s _ _ _ _ _ * * *	_ C L	No signalling, Squelch off
Sensitivity	-	S e n s _ _ _ _ _ * * *	_ _ C	No signalling, Squelch off
Sensitivity	-	S e n s _ _ _ _ _ * * *	_ H C	No signalling, Squelch off
Sensitivity	-	S e n s _ _ _ _ _ * * *	_ _ H	No signalling, Squelch off
Squelch tight	Wide	S q l _ T _ _ _ _ _ * * *		No signalling
Squelch tight	Narrow	S q l _ T _ _ _ _ _ * * *	n _ _	No signalling
Squelch open	Wide	S q l _ O _ _ _ _ _ * * *		No signalling
Squelch open	Narrow	S q l _ O _ _ _ _ _ * * *	n _ _	No signalling

ADJUSTMENT

3-3. Flow Chart

1. ↔ : Use the [PF1] and [PF2] keys.
2. → : Use the [PF6] key.
3. Use the [SELECTOR] knob to set an adjustment value (1 to 256) for each adjustment item.
4. Use the [PF6] key to move to the next item.

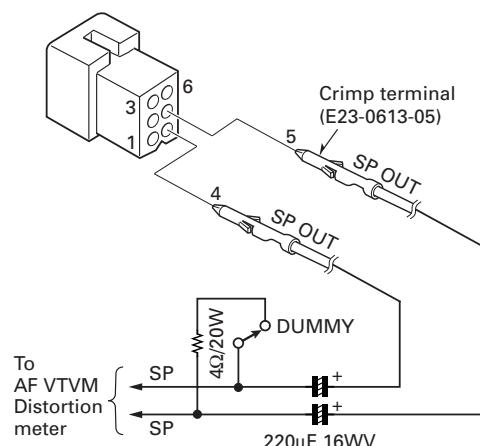


ADJUSTMENT

4. Test Equipment Required for Alignment

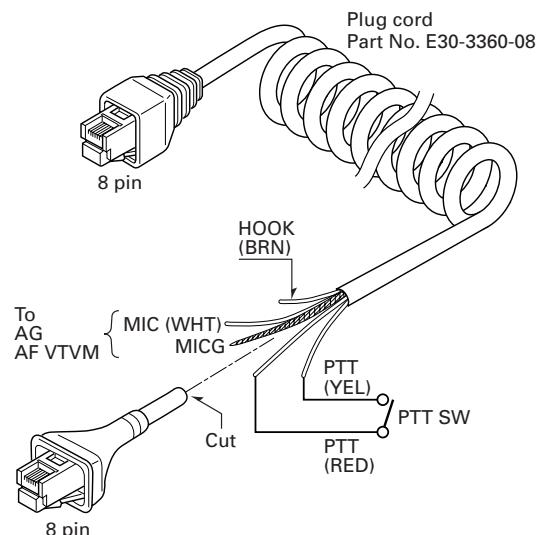
Test Equipment	Major Specifications	
1. Standard Signal Generator (SSG)	Frequency Range	400 to 512MHz
	Modulation	Frequency modulation and external modulation
	Output	0.1μV to greater than 1mV
2. Power Meter	Input Impedance	50Ω
	Operation Frequency	400 to 512MHz or more
	Measurement Capability	Vicinity of 50W
3. Deviation Meter	Frequency Range	400 to 512MHz
4. Digital Volt Meter (DVM)	Measuring Range	1 to 20V DC
	Accuracy	High input impedance for minimum circuit loading
5. Oscilloscope		DC through 30MHz
6. High Sensitivity Frequency Counter	Frequency Range	10Hz to 600MHz
	Frequency Stability	0.2ppm or less
7. Ammeter		13A or more
8. AF Volt Meter (AF VTVM)	Frequency Range	50Hz to 10kHz
	Voltage Range	3mV to 3V
9. Audio Generator (AG)	Frequency Range	50Hz to 5kHz
	Output	0 to 1V
10. Distortion Meter	Capability	3% or less at 1kHz
	Input Level	50mV to 10Vrms
11. Voltmeter	Measuring Range	10 to 1.5V DC or less
	Input Impedance	50kΩ/V or greater
12. 4Ω Dummy Load		Approx. 4Ω, 20W
13. Regulated Power Supply		13.6V, approx. 20A (adjustable from 9 to 20V) Useful if ammeter required

4-1. Test Cable for Speaker Output



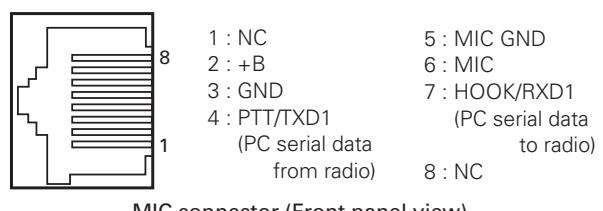
4-3. Test Cable for Microphone input

The following test cable are recommended.



4-2. Repair Jig (Chassis)

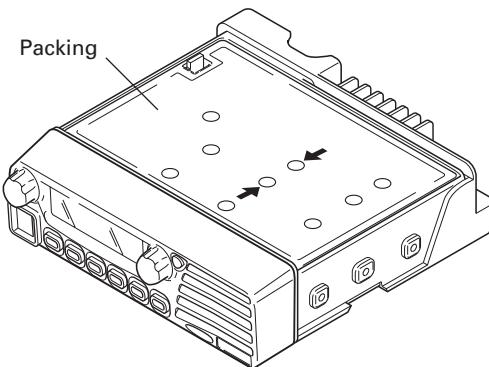
To check the voltage on the foil side of the TX-RX unit or to adjust the PLL lock voltage, use the Repair jig (Part No. A10-4065-02).



ADJUSTMENT

5. Warnings When Removing or Installing the Shielding Cover

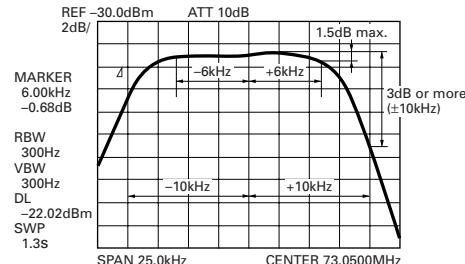
- When handling with the shielding cover, do not damage the components on the TX-RX unit.
- When installing the shielding cover, insert the cover from the rear side.
- When removing the shielding cover, squeeze the hole marked with an arrow as shown on the diagram and pull it straight up.



6. Common Section

Item	Condition	Measurement			Adjustment		Specifications/Remarks																																																																						
		Test-equipment	Unit	Terminal	Unit	Parts																																																																							
1. Frequency list	<ul style="list-style-type: none"> Frequency range (MHz) K : 450~500 	<ul style="list-style-type: none"> Adjustment frequency (MHz) <table border="1"> <thead> <tr> <th>CH</th> <th>RX</th> <th>TX</th> </tr> </thead> <tbody> <tr><td>1/FC</td><td>475.15</td><td>475.00</td></tr> <tr><td>2/FL</td><td>450.15</td><td>450.00</td></tr> <tr><td>3/FH</td><td>499.85</td><td>500.00</td></tr> <tr><td>4</td><td>462.55</td><td>462.50</td></tr> <tr><td>5</td><td>487.55</td><td>487.50</td></tr> <tr><td>6</td><td>475.00</td><td>475.00</td></tr> <tr><td>7</td><td>475.20</td><td>475.20</td></tr> <tr><td>8</td><td>475.40</td><td>475.40</td></tr> </tbody> </table>			CH	RX		TX	1/FC	475.15	475.00	2/FL	450.15	450.00	3/FH	499.85	500.00	4	462.55	462.50	5	487.55	487.50	6	475.00	475.00	7	475.20	475.20	8	475.40	475.40	<ul style="list-style-type: none"> Signalling <table border="1"> <thead> <tr> <th>No.</th> <th>Decode</th> <th>Encode</th> </tr> </thead> <tbody> <tr><td>1</td><td>None</td><td>None</td></tr> <tr><td>2</td><td>LTR</td><td>LTR</td></tr> <tr><td>3</td><td>QT 67Hz</td><td>QT 67Hz</td></tr> <tr><td>4</td><td>QT 151.4Hz</td><td>QT 151.4Hz</td></tr> <tr><td>5</td><td>QT 210.7Hz</td><td>QT 210.7Hz</td></tr> <tr><td>6</td><td>QT 254.1Hz</td><td>QT 254.1Hz</td></tr> <tr><td>7</td><td>DQT 023N</td><td>DQT 023N</td></tr> <tr><td>8</td><td>DQT 754I</td><td>DQT 754I</td></tr> <tr><td>9</td><td>DTMF decode (159D)</td><td>DTMF encode (159D)</td></tr> <tr><td>10</td><td>2-Tone decode A : 304.7Hz, B : 3106.0Hz</td><td>2-Tone encode A : 304.7Hz, B : 3106.0Hz</td></tr> <tr><td>11</td><td>Single tone 979.9Hz</td><td>Single tone 979.9Hz</td></tr> <tr><td>12</td><td>MSK</td><td>MSK</td></tr> <tr><td>13</td><td>None</td><td>Single tone : 1kHz</td></tr> </tbody> </table>				No.	Decode	Encode	1	None	None	2	LTR	LTR	3	QT 67Hz	QT 67Hz	4	QT 151.4Hz	QT 151.4Hz	5	QT 210.7Hz	QT 210.7Hz	6	QT 254.1Hz	QT 254.1Hz	7	DQT 023N	DQT 023N	8	DQT 754I	DQT 754I	9	DTMF decode (159D)	DTMF encode (159D)	10	2-Tone decode A : 304.7Hz, B : 3106.0Hz	2-Tone encode A : 304.7Hz, B : 3106.0Hz	11	Single tone 979.9Hz	Single tone 979.9Hz	12	MSK	MSK	13	None
CH	RX	TX																																																																											
1/FC	475.15	475.00																																																																											
2/FL	450.15	450.00																																																																											
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12	MSK	MSK																																																																											
13	None	Single tone : 1kHz																																																																											
2. Setting	<ul style="list-style-type: none"> Receiver section The indicated SSG output level are for maximum output. Whenever there is no modulation designation, standard modulation (MOD : 1kHz, DEV : 3kHz (Wide), DEV : 1.5kHz (Narrow)) is indicated. 	<ul style="list-style-type: none"> Transmitter section 						<p>When adjusting the PLL lock voltage, you must set the TX POWER value to 0.</p> <p>If you transmit without connecting a 50Ω terminator on the antenna connector, the FET final transistor may be damaged.</p>																																																																					

ADJUSTMENT

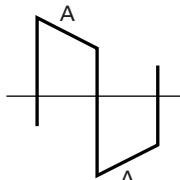
Item	Condition	Measurement			Adjustment			Specifications/Remarks	
		Test-equipment	Unit	Terminal	Unit	Parts	Method		
3. PLL lock voltage	1) CH-SIG : 3-1	DC VM Power meter	TX-RX	CV	TX-RX	TC502	8.2V	$\pm 0.1V$	
	2) CH-SIG : 3-1 PTT : ON					TC501			
	3) CH-SIG : 2-1					Check	2.0V or more		
	4) CH-SIG : 2-1 PTT : ON								
4. Transmit frequency	1) CH-SIG : 1-1 Select Freq_____*** in tuning mode PTT : ON	Power meter f. counter	Rear panel	ANT	Front panel	Selector knob	CH-1 frequency	$\pm 50Hz$	
5. MCF • Wide	1) CH-SIG : 1-1 Spectrum analyzer Span : 25kHz Scale : 2dB div Center frequency : 73.05MHz Tracking generator Output : -30dBm	Spectrum analyzer Tracking generator	TX-RX	CN201	TX-RX	L205 L207 L209	Adjust it to make gain maximum, and make the band flat as shown in the below.		
	L206 L208 L210					 <p>REF -30.0dBm ATT 10dB 2dB/ 10kHz MARKER 6.00kHz -0.68dB RBW 300Hz VBW 300Hz DL -22.02dBm SWP 1.3s SPAN 25.0kHz CENTER 73.0500MHz</p>			
• Narrow	1) [PF5] key : Set the narrow "n_ _" appear on the LCD					Adjust it to make gain maximum, and make the band flat as shown in the below.			
6. AF coil adjustment	1) CH-SIG : 1-1 SSG output : -20dBm (FM) frequency : 73.05MHz DEV : 1.5kHz MOD : 1kHz SSG signal inject to CN201.	SSG AF VTVM 4Ω dummy load	TX-RX	CN201	TX-RX	L201	Maximum audio output level.		

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ADJUSTMENT

Item	Condition	Measurement			Adjustment			Specifications/Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
7. TX high power	1) CH-SIG : 3-1 Select __H Hi_Power_*** in tuning mode PTT : ON Keep data at 256	Power meter Ammeter	Rear panel	ANT	TX-RX	VR1	48W	48W±0.5W
	2) CH-SIG : 2-1 Select __L Hi_Power_*** in tuning mode PTT : ON							45W±1W 12A or less
	3) CH-SIG : 1-1 Select __C Hi_Power_*** in tuning mode PTT : ON							
	4) CH-SIG : 3-1 Select __H Hi_Power_*** in tuning mode PTT : ON							
8. TX low power	1) CH-SIG : 2-1 Select __L Lo_Power_*** in tuning mode PTT : ON				Front panel	Selector knob	15W	15W±1W 12A or less
	2) CH-SIG : 1-1 Select __C Lo_Power_*** in tuning mode PTT : ON							
	3) CH-SIG : 3-1 Select __H Lo_Power_*** in tuning mode PTT : ON							
9. Maximum deviation • Wide	1) CH-SIG : 2-1 Select __L Max_Dev_*** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 15kHz De-emphasis : OFF AG : 1kHz/50mV	Power meter Deviation meter AF VTVM AG Oscilloscope	Rear panel Front panel	ANT MIC	Front panel	Selector knob	±3.8kHz	±0.1kHz
	2) CH-SIG : 1-1 Select __C Max_Dev_*** in tuning mode PTT : ON							±3.8kHz
	3) CH-SIG : 3-1 Select __H Max_Dev_*** in tuning mode PTT : ON							±0.1kHz

ADJUSTMENT

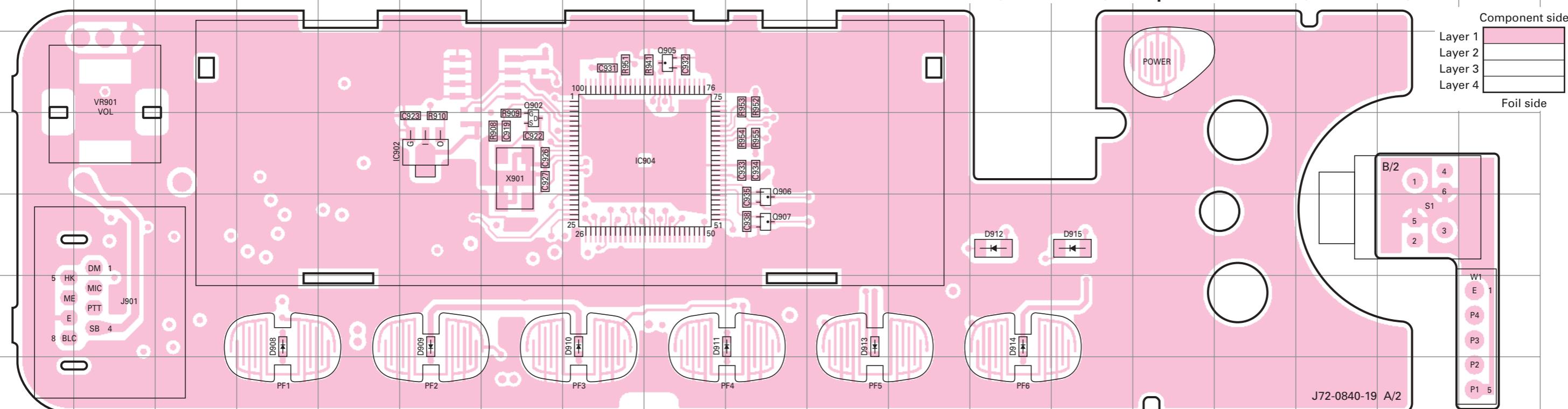
Item	Condition	Measurement			Adjustment			Specifications/Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
• Narrow	4) CH-SIG : 2-1 [PF5] key : Set the narrow n_L Max_Dev_ _*** in tuning mode PTT : ON	Power meter Deviation meter AF VTVM AG Oscilloscope	Rear panel Front panel	ANT MIC	Front panel	Selector knob	±1.75kHz	±0.05kHz
	5) CH-SIG : 1-1 Select n_C Max_Dev_ _*** in tuning mode PTT : ON							
	6) CH-SIG : 3-1 Select n_H Max_Dev_ _*** in tuning mode PTT : ON							
10. MIC seisitivity (Wide only)	1) CH-SIG : 1-1 PTT : ON Deviation meter filter HPF : OFF LPF : 15kHz De-emphasis : OFF AG : 1kHz/5mV		TX-RX	VR700	Check		2.2~3.7kHz	
11. DQT balance • Wide	1) CH-SIG : 1-1 Select DQT_Bal_ _*** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 3kHz De-emphasis : OFF							
	2) [PF5] key : Set the narrow n_ _ DQT_Bal_ _*** in tuning mode PTT : ON	Front panel	Selector knob	Wide/Narrow Make the demodulation wave- form neat.			Flat the A parts. 	
12. QT deviation • Wide	1) CH-SIG : 1-3 Select QT_Dev_ _ _ *** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 3kHz De-emphasis : OFF				±0.75kHz		±0.05kHz	
	2) [PF5] key : Set the narrow n_ _ QT_Dev_ _ _ *** in tuning mode PTT : ON							
• Narrow					±0.35kHz		±0.05kHz	

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ADJUSTMENT

Item	Condition	Measurement			Adjustment			Specifications/Remarks
		Test-equipment	Unit	Terminal	Unit	Parts	Method	
13. DQT deviation • Wide	1) CH-SIG : 1-7 Select DQT_Dev_ _*** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 3kHz De-emphasis : OFF	Power meter Deviation meter AF VTVM AG Oscilloscope	Rear panel Front panel	ANT MIC	Front panel	Selector knob	±0.75kHz	±0.05kHz
	2) [PF5] key : Set the narrow n_ _ DQT_Dev_ _*** in tuning mode PTT : ON						±0.35kHz	±0.05kHz
14. LTR code deviation • Wide	1) CH-SIG : 1-2 Select LTR_Dev_ _*** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 3kHz De-emphasis : OFF						1.00kHz	±0.05kHz
	2) [PF5] key : Set the narrow n_ _ LTR_Dev_ _*** in tuning mode PTT : ON						0.75kHz	±0.05kHz
15. DTMF deviation • Wide	1) CH-SIG : 1-9 Select DTMF_Dev_ _*** in tuning mode PTT : ON Deviation meter filter HPF : OFF, LPF : 15kHz De-emphasis : OFF						±3.0kHz	±0.1kHz
	2) [PF5] key : Set the narrow n_ _ DTMF_Dev_ _*** in tuning mode PTT : ON						±1.5kHz	±0.1kHz
16. MSK deviation • Wide	1) CH-SIG : 1-12 Select MSK_Dev_ _*** in tuning mode PTT : ON Deviation meter filter HPF : OFF LPF : 15kHz De-emphasis : OFF						±3.0kHz	±0.1kHz
	2) [PF5] key : Set the narrow n_ _ MSK_Dev_ _*** in tuning mode PTT : ON						±1.5kHz	±0.05kHz

TK-8150 PC BOARD

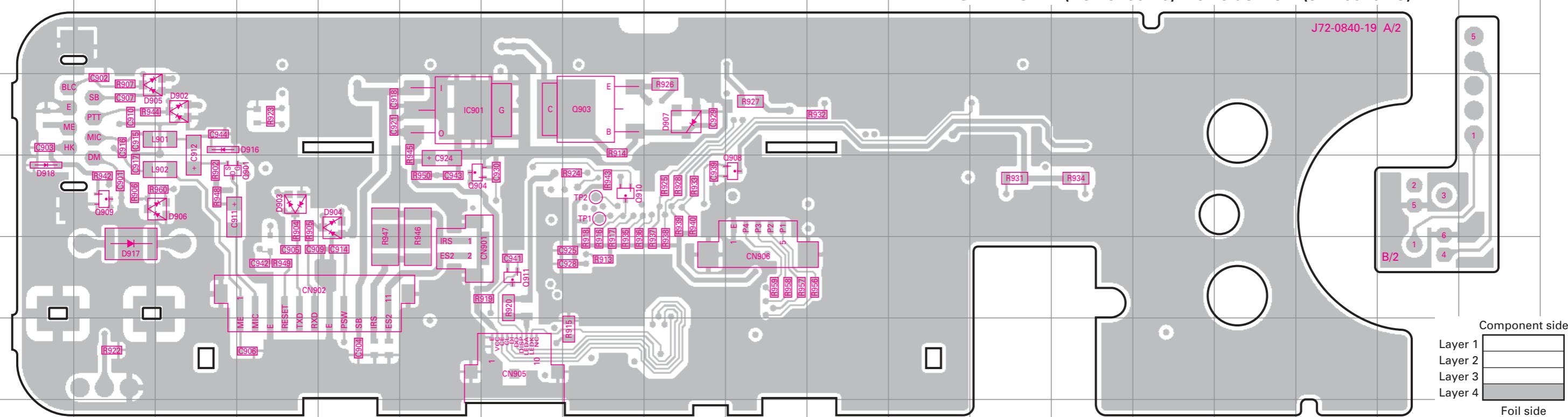


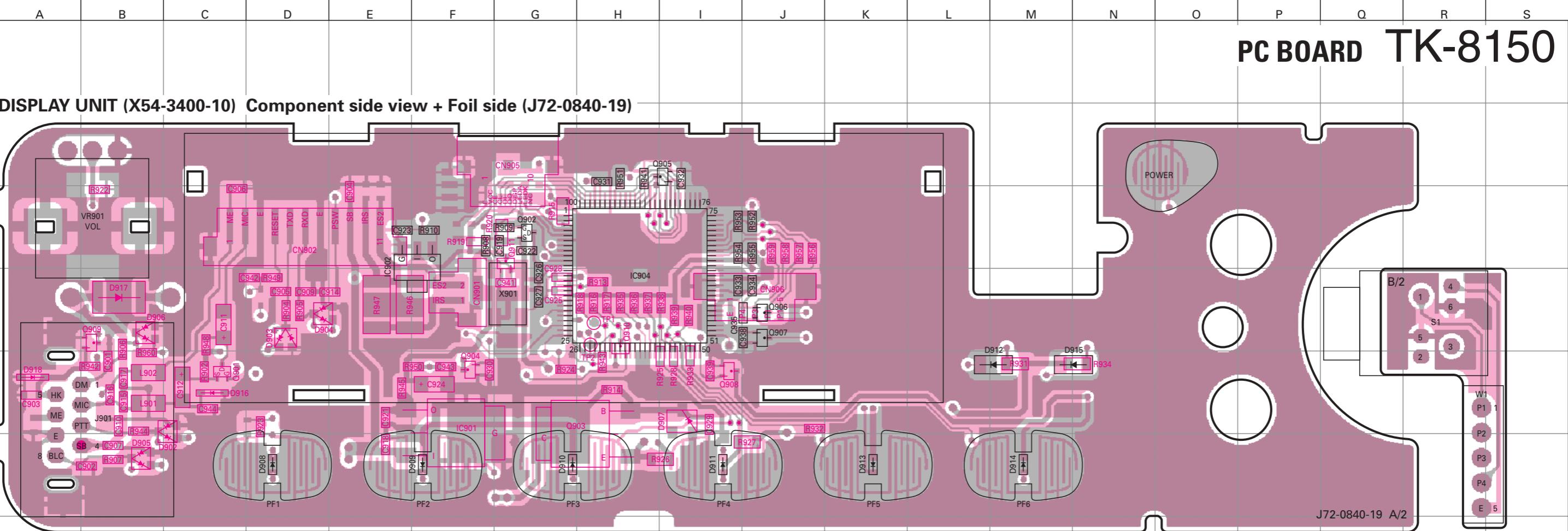
DISPLAY UNIT (X54-3400-10) Foil side view (J72-0840-19)

J72-0840-19 A/2

Ref. No. Address Ref. No. Address

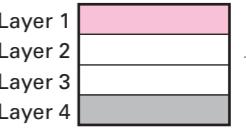
IC901	9F	Q908	10J	D902	9C	D906	10C	D918	10A
Q901	10C	Q909	10B	D903	10D	D907	9I		
Q903	9H	Q910	10H	D904	10E	D916	9C		
Q904	10F	Q911	11G	D905	9B	D917	11B		





Ref. No.	Address						
IC901	5F	Q906	4J	D904	4D	D912	5M
IC902	3F	Q907	4J	D905	6B	D913	6K
IC904	4H	Q908	5I	D906	4B	D914	6M
Q901	5C	Q909	4B	D907	5I	D915	5N
Q902	3G	Q910	4H	D908	6D	D916	5C
Q903	5H	Q911	3G	D909	6F	D917	4B
Q904	5F	D902	5C	D910	6G	D918	5A
Q905	2I	D903	4D	D911	6I		

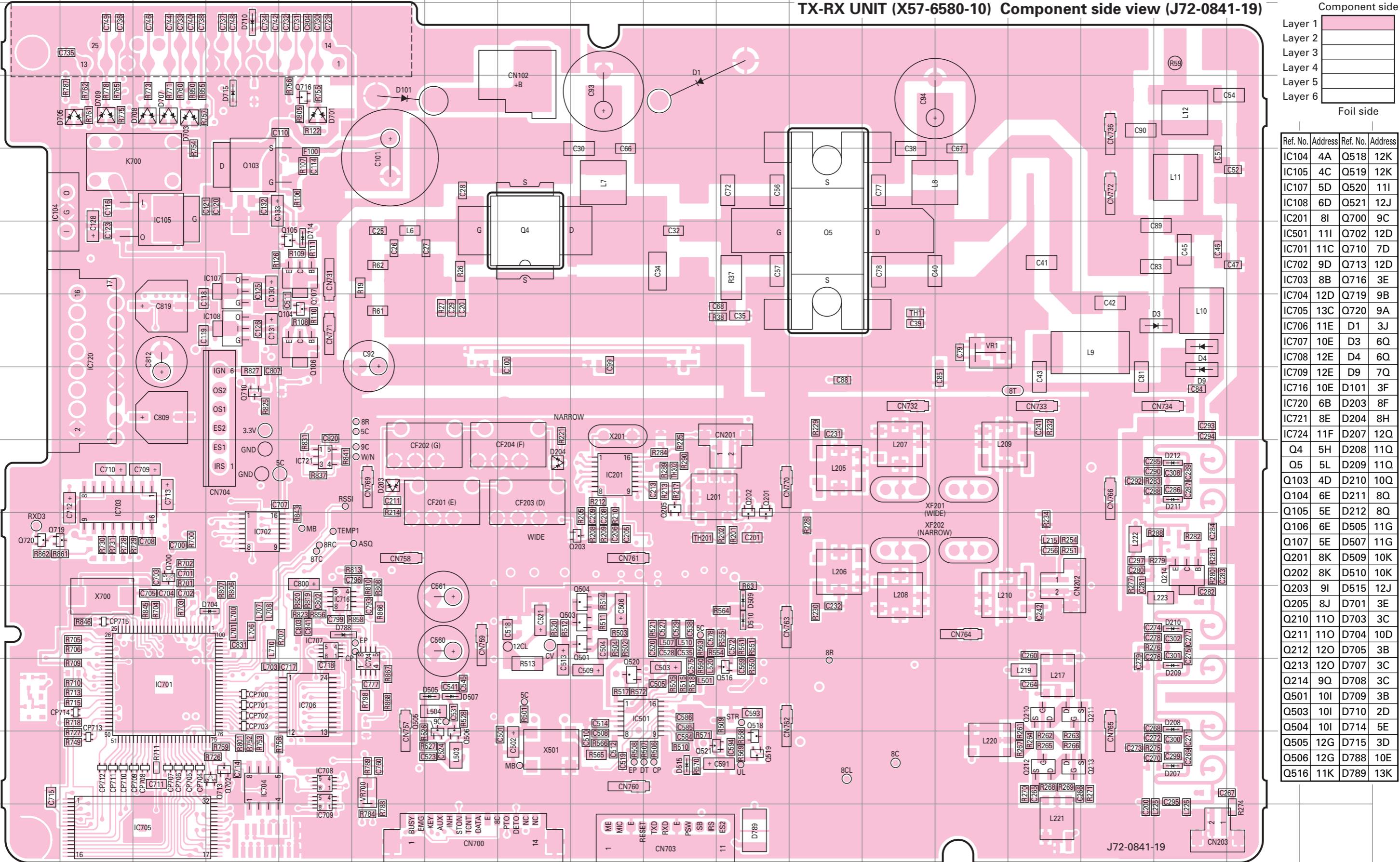
Component side



Foil side

• Connect 1 and 4

TK-8150 PC BOARD



Component side

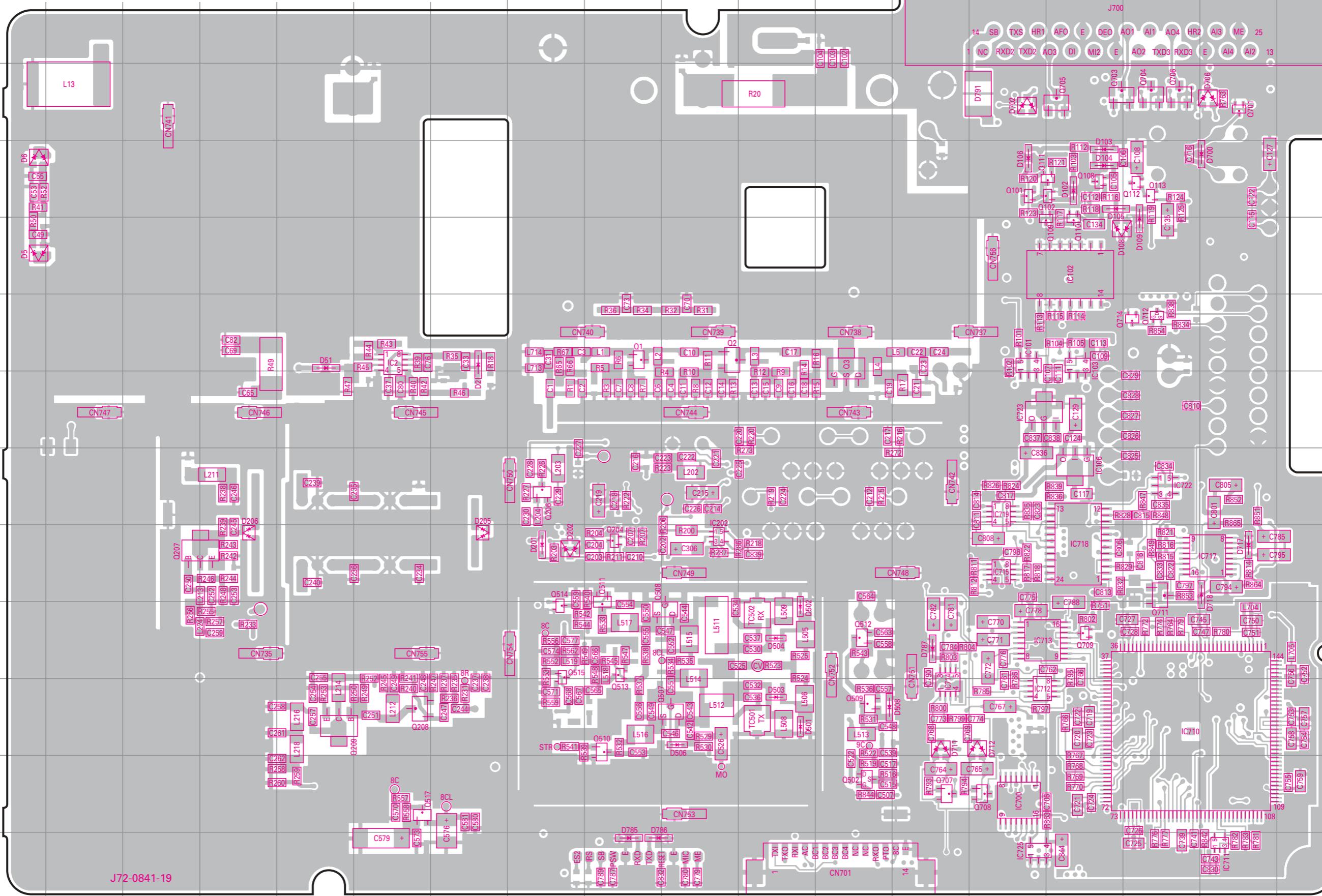
Layer 1
Layer 2
Layer 3
Layer 4
Layer 5
Layer 6

Foil side

Ref. No. Address Ref. No. Address

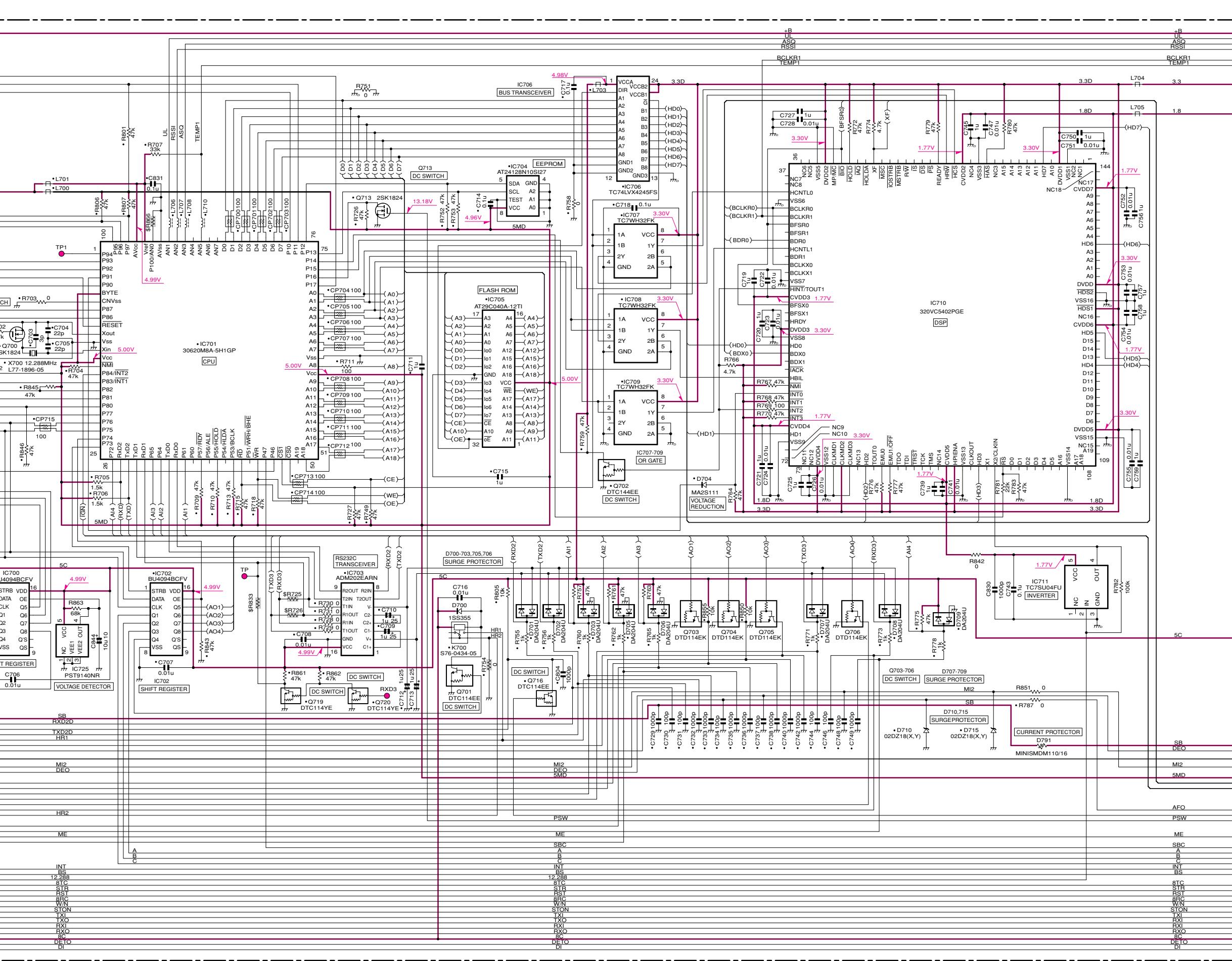
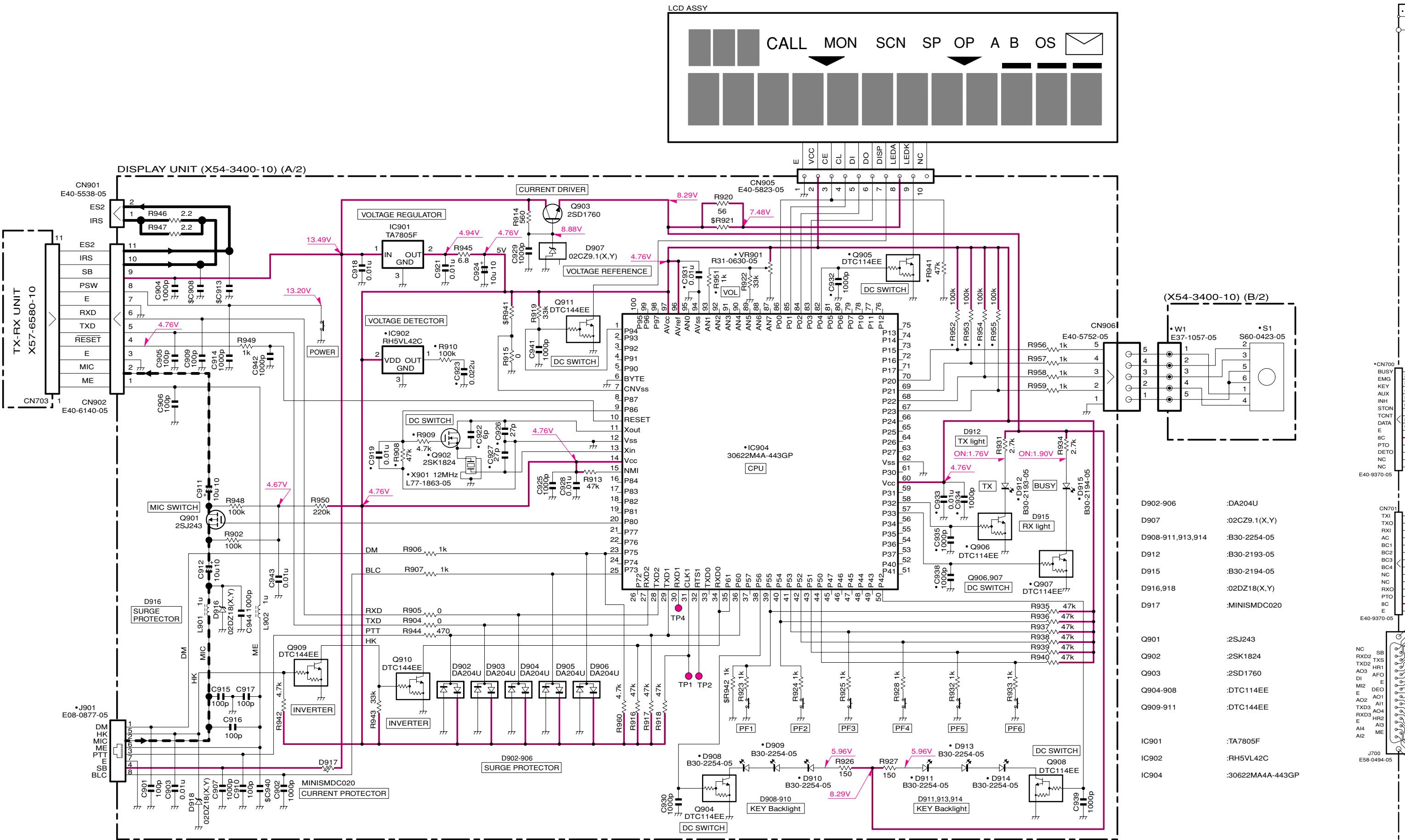
IC2	6G	Q517	12G
IC101	6O	Q701	3R
IC102	5P	Q703	3P
IC103	6P	Q704	3Q
IC106	8P	Q705	3P
IC202	9K	Q706	3Q
IC700	12O	Q707	12N
IC710	11Q	Q708	12O
IC711	13R	Q709	10P
IC712	11O	Q711	9Q
IC713	10O	Q712	6Q
IC714	11N	Q714	6Q
IC715	9O	D2	6H
IC717	9R	D5	5B
IC718	9P	D6	4B
IC719	8O	D51	6F
IC722	8Q	D102	4P
IC723	7O	D103	4P
IC725	13O	D104	4P
Q1	6J	D105	4P
Q2	6K	D106	4O
Q3	6M	D108	5P
Q101	4O	D109	5Q
Q102	4P	D201	9I
Q108	4P	D202	9I
Q109	5P	D205	9H
Q110	5P	D206	9E
Q111	4P	D501	11L
Q112	4Q	D502	10L
Q113	4Q	D503	11L
Q204	9J	D504	10L
Q206	8I	D506	11K
Q207	9D	D508	11M
Q208	11G	D700	4R
Q209	11F	D702	3O
Q502	12M	D706	3R
Q507	11K	D711	11N
Q508	10K	D712	11O
Q509	11M	D717	9R
Q510	11J	D718	9R
Q511	9J	D785	13J
Q512	10M	D786	13J
Q513	10J	D787	10N
Q514	10I	D791	3O
Q515	10I		

TX-RX UNIT (X57-6580-10) Foil side view (J72-0841-19)



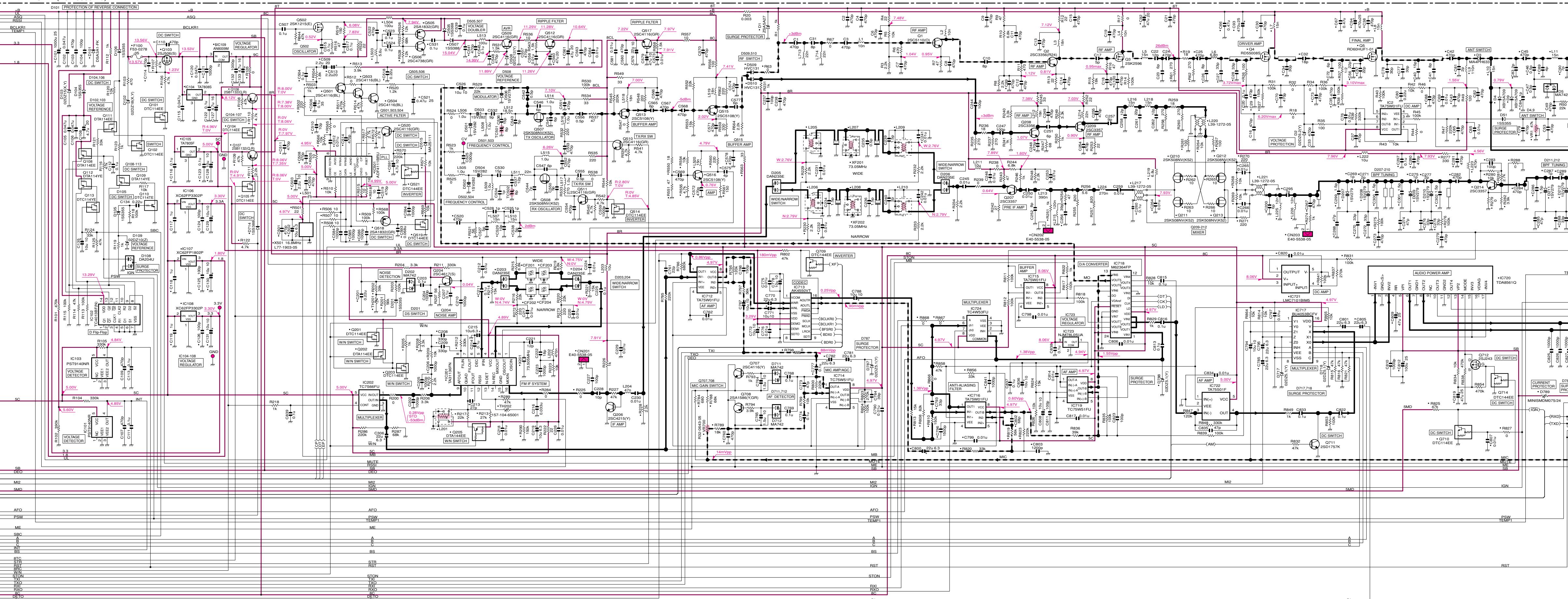
SCHEMATIC DIAGRAM TK-8150

Note : Components marked with a dot (•) are parts of layer 1.



TK-8150 SCHEMATIC DIAGRAM

TX-RX UNIT (X57-6580-10)

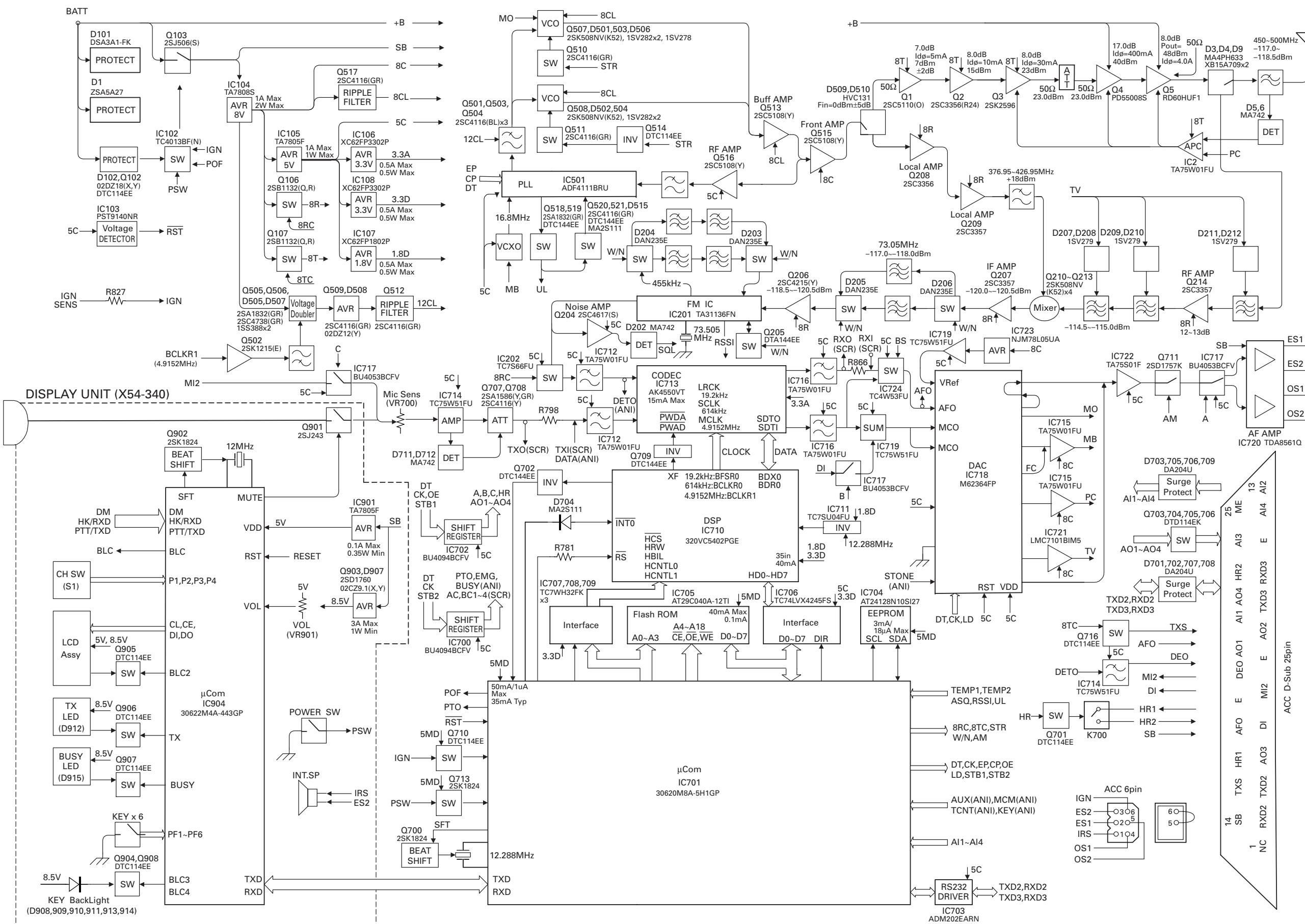


Note : Components marked with a dot (-) are parts of layer 1.

D1	:ZSA5A27
D2	:02DZ6.2(Y)
D3	:IM4A4PH633
D4.9	:XB15A709
D5.6	:MA742
D51.104	:1SS355
D101	:DSA5A1-FK
D102	:02DZ18(X,Y)
D103	:02DZ10(Z)
D108.701	:DAN235E
D109	:1SV279
D203.206	:1SV282
D207.212	:1SS388
D501.504	:02DZ12(Y)
D505.507	:HVC131
D506	:MA2S11
D509.510	:D717.718,785-788
D515.704	:02DZ5.1(VY)
D789	:MINISMDM075/24
D791	:2SC5110(O)
Q1	:2SC5336(R24)
Q2	:2SK2596
Q3	:RD50008S
Q4	:RD601HF1-01
Q5	:DTC114EE
Q101.102.104.105.	:2SJ506(S)
Q101.203.514.	:2SB1132(Q,R)
Q103	:DTA114EE
Q106.107	:DTA114YE
Q108.111.202	:DTA114YE
Q109.112	:DTA114TE
Q110	:DTA114YE
Q113.719.720	:DTA114YE
Q204	:DTC114TE
Q205	:DTC114YE
Q206	:DTC114YE
Q207.209.214	:DTC114YE
Q208	:2SC3357
Q210.213.507.508	:2SC508NV(K52)
Q501.503.504	:2SC416(BL)
Q505.518	:2SK1215(E)
Q506	:2SA1832(GR)
Q509.512.517.520	:2SC416(GR)
Q513.515.516	:2SC5108(Y)
Q519.521.702.709.714	:DTC144EE
Q700.713	:2SK1824
Q703.706	:DTD114EK
Q707	:2SC416(Y)
Q708	:2SA1586(Y,GR)
Q711	:2SD1757K
Q712	:2SJ243
IC2.712.715.716	:TA75W01FU
IC101.103.725	:PST9140NR
IC102	:TC4013BF(N)
IC104	:TA7808S
IC105	:TA7805F
IC106.108	:XC62F2P1802P
IC107	:TA31136FN
IC201	:TC7566FU
IC501	:ADF4111BRU
IC700.702	:BUJ4094BCFV
IC703	:30620M8A-5H1GP
IC704	:ADM202EARN
IC705	:AT24128N10SI2
IC706	:AT29C04A-12TI
IC707.709	:TC7WH32FK
IC710	:320VCS402PGE
IC711	:TC7SU04FU
IC713	:AK4550VT
IC714.719	:TC75W51FU
IC717	:BUJ405BCFV
IC718	:M62364FP
IC720	:TDA8561Q
IC721	:LMC7101BIMS
IC722	:TA75S01F
IC723	:INJM78L05UA
IC724	:TC4W53FU

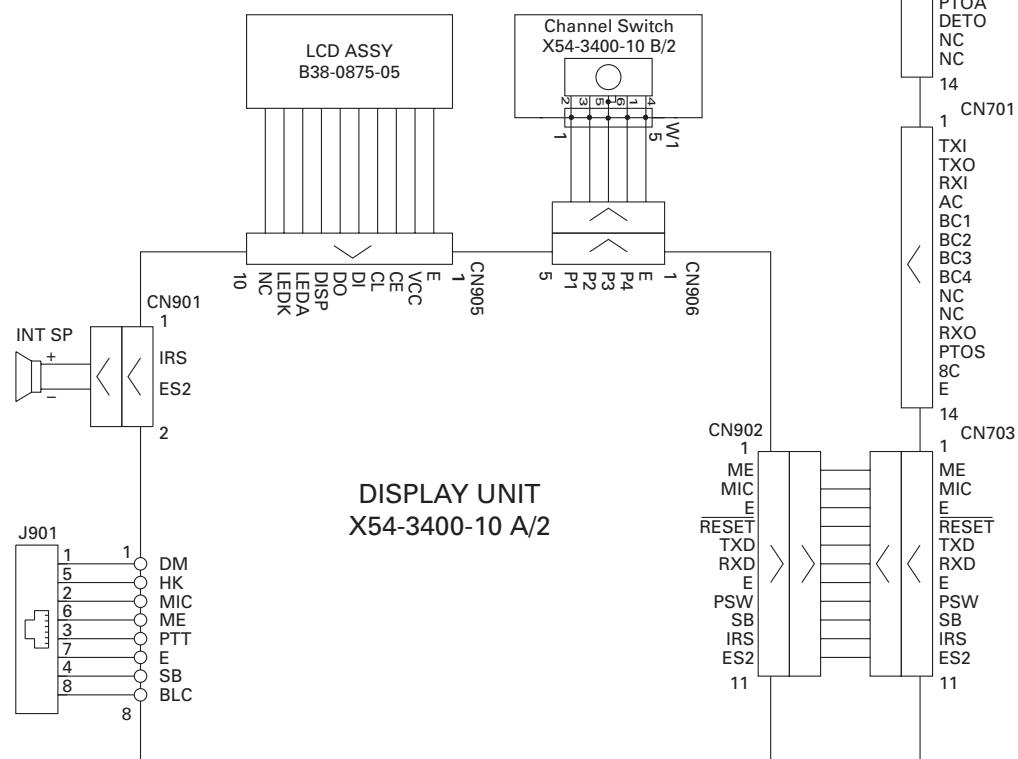
TK-8150 TK-8150

BLOCK DIAGRAM

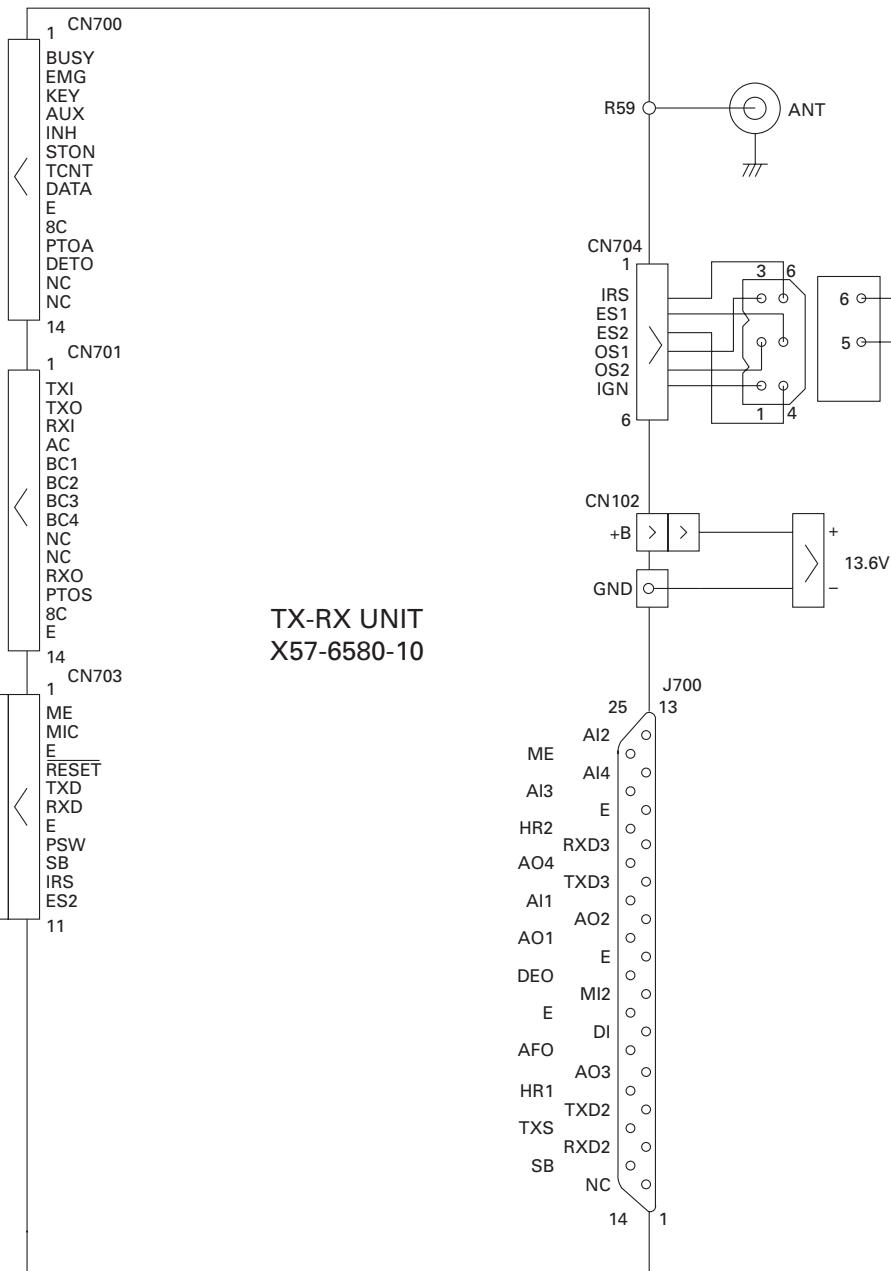


WIRING

TK-8150



DISPLAY UNIT
X54-3400-10 A/2



TERMINAL FUNCTION

Pin No.	Name	Description
11	PTOA	PTT signal output. "L" : PTT on, "H" : PTT off $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
12	DETO	Detected signal output. DC coupled. $Z_o \leq 100\Omega$ Wide : $0.8V_{p-p}$ typ., Narrow : $0.7V_{p-p}$ typ. (Standard modulation)
13	NC	Non connection.
14	NC	Non connection.
CN701 (To voice scrambler)		
1	TXI	MIC signal input (Capacitor-coupled). $Z_{in} = 100k\Omega$
2	TXO	MIC signal output (Capacitor-coupled). $Z_o = 1k\Omega$ Wide : $85mV_{p-p}$ typ., Narrow : $85mV_{p-p}$ typ. (Standard modulation)
3	RXI	Audio signal input (Capacitor-coupled). $Z_{in} = 27k\Omega$ Wide : $1.3V_{p-p}$ typ., Narrow : $1.1V_{p-p}$ typ. (Standard modulation)
4	AC	Scrambler control signal output. "L" : On, "H" : Off $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
5	BC1	Scrambler code signal output 1. $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
6	BC2	Scrambler code signal output 2. $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
7	BC3	Scrambler code signal output 3. $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
8	BC4	Scrambler code signal output 4. $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
9	NC	Non connection.
10	NC	Non connection.
11	RXO	Audio signal output. DC coupled. $Z_o \leq 100\Omega$ Wide : $1.3V_{p-p}$ typ., Narrow : $1.1V_{p-p}$ typ. (Standard modulation)
12	PTOS	TX signal output. "L" : TX, "H" : Not TX $L \leq 0.05V$, $H \geq 4.6V/30k\Omega$ load
13	8C	8V. (CN700 No.10 + CN701 No.13 = 100mA Max.)
14	E	Ground.
CN703 (To Display unit A/2)		
1	ME	MIC ground.
2	MIC	MIC signal input.
3	E	Ground.
4	<u>RESET</u>	Reset signal output.
5	TXD	Serial data output.

Pin No.	Name	Description
6	RXD	Serial data input.
7	E	Ground.
8	PSW	Power switch control signal input.
9	SB	Power output after power switch ($13.6V \pm 15\%$).
10	IRS	BTL output for remote speaker output.
11	ES2	BTL output for remote speaker output.
CN704 (To ACC 6-pin connector)		
1	IRS	Remote speaker switch.
2	ES1	BTL output for external speaker A.
3	ES2	BTL output for external speaker A.
4	OS1	BTL output for external speaker B (PA).
5	OS2	BTL output for external speaker B (PA).
6	IGN	Ignition sense input.
J700 (ACC 25-pin)		
1	NC	Non connection.
2	RXD2 *	Serial data input 2. RS-232C level. Input voltage range = $\pm 30V$ max. $L \leq 0.4V$, $H \geq 2.4V$. $Z_i \geq 5k\Omega$
3	TXD2 *	Serial data output 2. RS-232C level. $L \leq -5V$, $H \geq 5V/3k\Omega$ load. $Z_o \leq 2k\Omega$
4	AO3	Auxiliary output 3 (Programmable). Active low open collector (100mA max.) (Default none). $L \leq 0.3V$, $H \geq 4.8V$
5	DI	Data signal input (Capacitor-coupled). $Z \geq 10k\Omega$
6	MI2	External MIC input (Capacitor-coupled). $Z = 600\Omega$
7	E	Ground.
8	AO2	Auxiliary output 2 (Programmable). Active low with $10k\Omega$ pull-up to 5V (100mA max.) (Default none). $L \leq 0.3V$, $H \geq 4.8V$
9	TXD3 **	Serial data output 3. $L \leq 0.7V$, $H \geq 4.2V/25k\Omega$ load. $Z_o \leq 1k\Omega$
10	RXD3 **	Serial data input 3. Input voltage range = $+5/0$ max. $L \leq 0.8V$, $H \geq 4.2V$.
11	E	Ground.
12	AI4	Auxiliary input 4 (Programmable). Active low with $47k\Omega$ pull-up to 5V (Default none). $L \leq 0.8V$, $H \geq 2.5V$
13	AI2	Auxiliary input 2 (Programmable). Active low with $47k\Omega$ pull-up to 5V (Default none). $L \leq 0.8V$, $H \geq 2.5V$

TERMINAL FUNCTION / OPTIONS

Pin No.	Name	Description
14	SB	Power output after power switch. $13.6V \pm 15\%$, 1.1A max. at $25^{\circ}C$, 0.8A max. at $60^{\circ}C$
15	TXS	TX sense signal output. Active high with $10k\Omega$ pull-up to 5V. $L \leq 0.3V$, $H \geq 4.8V$
16	HR1	Horn alert signal output 1. 1A max.
17	AFO	Filtered audio output (DC-coupled). $Z_o \leq 1k\Omega$
		PA : MIC signal output. 0.65Vp-p typ. (1kHz/5mV input)
		RX : RX low level output. Wide : 1.3Vp-p typ., Narrow : 1.1Vp-p typ. (Standard modulation)
18	E	Ground.
19	DEO	Detected signal output (Capacitor-coupled).
20	AO1	Auxiliary output 1 (Programmable). Active low with $10k\Omega$ pull-up to 5V (100mA max.) (Default none). $L \leq 0.3V$, $H \geq 4.8V$
21	AI1	Auxiliary input 1 (Programmable). Active low with $47k\Omega$ pull-up to 5V (Default none). $L \leq 0.8V$, $H \geq 2.5V$
22	AO4	Auxiliary output 4 (Programmable). Active low open collector (100mA max.) (Default none). $L \leq 0.3V$
23	HR2	Horn alert signal output 2. 1A max. (Default ground)
24	AI3	Auxiliary input 3 (Programmable). Active low with $47k\Omega$ pull-up to 5V (Default none). $L \leq 0.8V$, $H \geq 2.5V$
25	ME	MIC ground.

* and ** : Pair

4. ACC 6-pin Connector

Pin No.	Name	Description
1	IGN	Ignition sense input.
2	OS2	BTL output for external speaker B (PA).
3	OS1	BTL output for external speaker B (PA).
4	ES2	BTL output for external speaker A.
5	ES1	BTL output for external speaker A.
6	RS1	Remote speaker switch.

1. KES-5 (External Speaker)

1-1. External View



1-2. Parts List

* : New parts

Ref. No.	New parts	Parts No.	Description
	*	A62-1050-08	Panel assy
	*	A82-0050-08	Rear panel
	*	B43-1142-08	Badge Panel
	*	E30-3479-08	Speaker cable
	*	G13-1923-08	Cushion SP front
	*	G53-1553-08	Packing Rear panel
	*	J29-0699-08	Bracket
	*	N08-0551-08	Dressed screw Bracket
	*	N99-2031-08	Screw set Acc
	*	T07-0741-08	Speaker

2. KRK-9 (Single Control Head Remote Kit)

1-1. External View



1-2. Parts List

* : New parts

Ref. No.	New parts	Parts No.	Description
	*	A62-1039-02	Panel Body
	*	A82-0049-02	Rear panel Head
	*	E37-1008-05	Lead wire with connector 11P
	*	F07-1865-03	Mold cover For cable
	*	G02-0895-04	Earth spring Cable GND
	*	G53-1552-03	Packing Rear panel
	*	J21-8447-04	Hardware fixture Panel
	*	J29-0698-03	Bracket Acc
	*	N08-0550-04	Dressed screw Acc
	*	N87-3008-45	Brazier head taprite screw Panel
	*	N99-2032-05	Screw set Acc

SPECIFICATIONS

GENERAL

Frequency Range	450~500MHz (K)		
Number of Channels (Zone)	Conventional CH : Max.128 (1 zone x 128ch~128 zones x 1ch) Trunking ID : Max. 250/zone (max. 32 zones)		
Channel Spacing	Wide : 25kHz	Narrow : 12.5kHz	
Operating Voltage	13.6V DC±15%		
Current Drain	Standby : 0.5A	Receive : 2.3A	Transmit : Less than 13A
Duty Cycle	Transmit : 20%		
Operating Temperature range	-22°F~+140°F (-30°C~+60°C)		
Frequency Stability (-22°F~+140°F)	±0.00025%		
Antenna Impedance	50Ω		
Dimensions (W x H x D)	7-1/12" x 2" x 7" (180mm x 50mm x 170mm)		
Weight (net)	5.9 lbs (2.7kg)		
Band Spread	50MHz		

RECEIVER

Sensitivity	12dB SINAD : 0.35µV	20dB Quieting : 0.45µV
Selectivity	Wide : 80dB (±25kHz)	Narrow : 73dB (±12.5kHz)
Intermodulation	Wide : 79dB (±25, 50kHz)	Narrow : 73dB (±12.5, 25kHz)
Spurious Response	85dB	
External Audio Output	12W (@4Ω 3% distortion), 13W (@4Ω 5% distortion)	
Internal Audio Output	3.2W (@16Ω 3% distortion), 3.5W (@16Ω 5% distortion)	

TRANSMITTER

RF Output Power	45 to 15 watts	High : 45 watts	Low : 15 watts
Type of Emission	Wide : 16K0F3E, 20K0F1D	Narrow : 11K0F3E, 11K2F1D	
Spurious Response	75dB		
FM Hum & Noise	Wide : 48dB	Narrow : 42dB	
Microphone Impedance	600Ω		
Audio Distortion	Less than 1% at 1000Hz		

Kenwood follows a policy of continuous advancement in development.

For this reason specifications may be changed without notice.

FleetSync™ is a trademark of Kenwood Corporation.

Applicable MIL-STD

Standard	MIL 810C Methods/Procedures	MIL 810D Methods/Procedures	MIL 810E Methods/Procedures	MIL 810F Methods/Procedures
Low Pressure	500.1/Procedure I	500.2/Procedure I, II	500.3/Procedure I, II	500.4/Procedure I, II
High Temperature	501.1/Procedure I, II	501.2/Procedure I, II Cat. A1	501.3/Procedure I, II Cat. A1	501.4/Procedure I, II
Low Temperature	502.1/Procedure I	502.2/Procedure I, II Cat. C1	502.3/Procedure I, II Cat. C1	502.4/Procedure I, II
Temperature Shock	503.1/Procedure I	503.2/Procedure I Cat. A1,C1	503.3/Procedure I Cat. A1,C1	503.4/Procedure I, II
Solar Radiation	505.1/Procedure I	505.2/Procedure I	505.3/Procedure I	505.4/Procedure I
Rain	506.1/Procedure II	506.2/Procedure II	506.3/Procedure II	506.4/Procedure III
Humidity	507.1/Procedure II	507.2/Procedure II	507.3/Procedure II	507.4
Salt Fog	509.1/Procedure I	509.2/Procedure I	509.3/Procedure I	509.4
Dust	510.1/Procedure I	510.2/Procedure I	510.3/Procedure I	510.4/Procedure I, III
Vibration	514.2/Procedure VIII, X	514.3/Procedure I Cat. 8	514.4/Procedure I Cat. 8	514.5/Procedure I Cat. 20
Shock	516.2/Procedure I, II, III, V	516.3/Procedure I, IV, V	516.4/Procedure I, IV, V	516.5/Procedure I, IV, V

